



Standby	
U12.0 V	
U3.3 V	
Power	
U5.0 V	
U3.3 V	
U2.5 V	
U1.8 V	
UDD	
HPS DDR VDD	
HPS DDR VTT	
FPGA DDR VDD	
FPGA DDR VTT	
STATUS	
RST	
FMC	
U12.0 V	
U3.3 V	
UADJ	
FPGA CONF	
HPS LED 1	
HPS LED 2	
FPGA LED 1	
FPGA LED 2	

 **ANALOG
DEVICES**

ARROW

 **trenz
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