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U_A-Headers
A-Headers.SchDoc



U_FPGA
FPGA.SchDoc



U_PowerSupply
PowerSupply.SchDoc



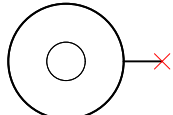
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FTDI.SchDoc



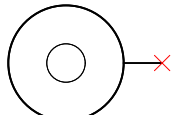
U_FPGA_MISC
FPGA_MISC.SchDoc



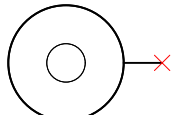
U_SENSOR_Flash_RAM
SENSOR_Flash_RAM.SchDoc



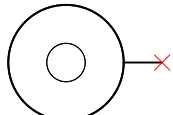
Mount.Hole 2mm (unplated)



Mount.Hole 2mm (unplated)

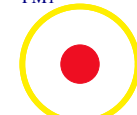


Mount.Hole 2mm (unplated)



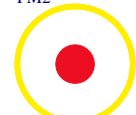
Mount.Hole 2mm (unplated)

PM1



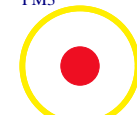
FIDU-DOT - mini

PM2



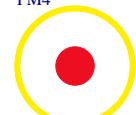
FIDU-DOT - mini

PM3



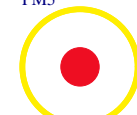
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PM4



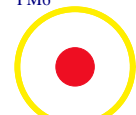
FIDU-DOT - mini

PM5



FIDU-DOT - mini

PM6



FIDU-DOT - mini

LOGO1



LOGO PRINT

Serial



Serialnumber 6,3 x 6.3mm



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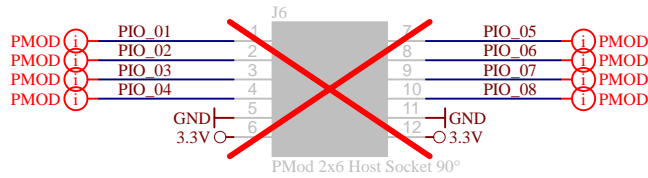
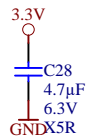
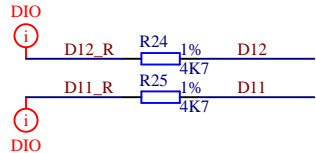
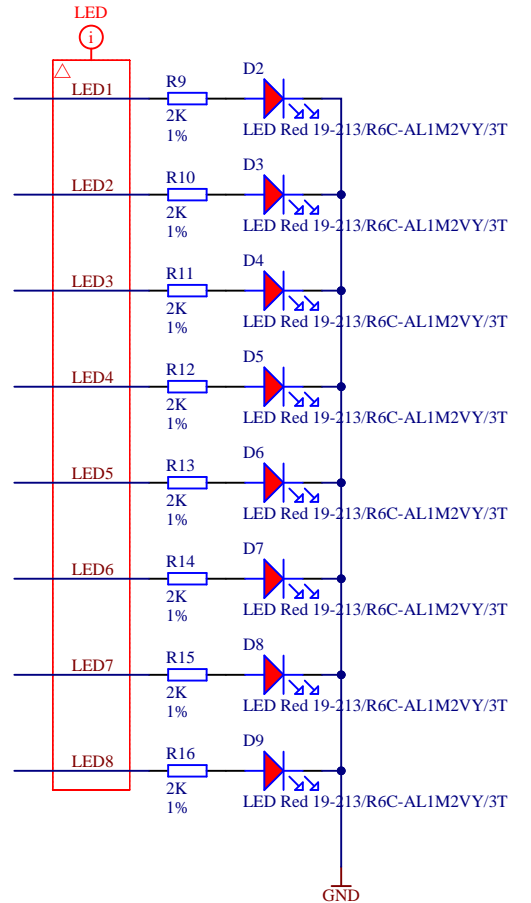
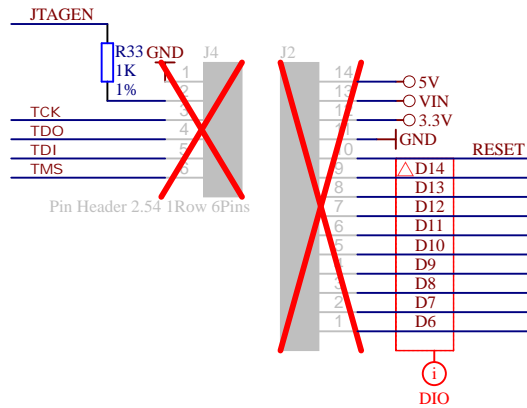
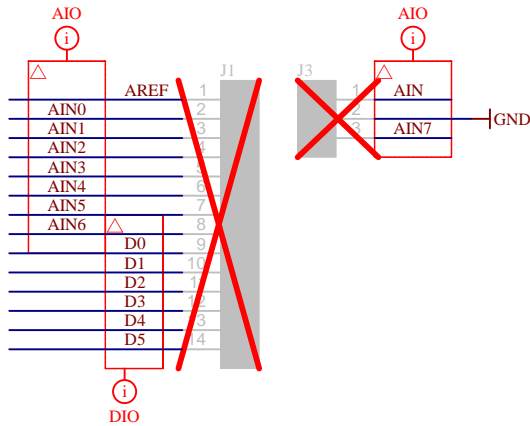
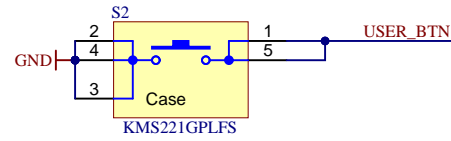
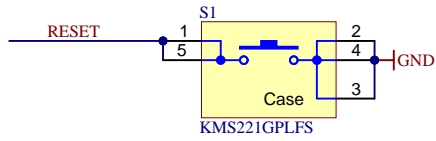
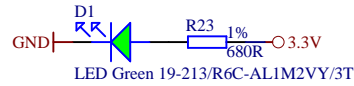
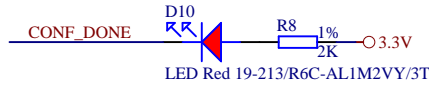
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DUAL Function: JTAG or USER I/O if FT2232H not installed



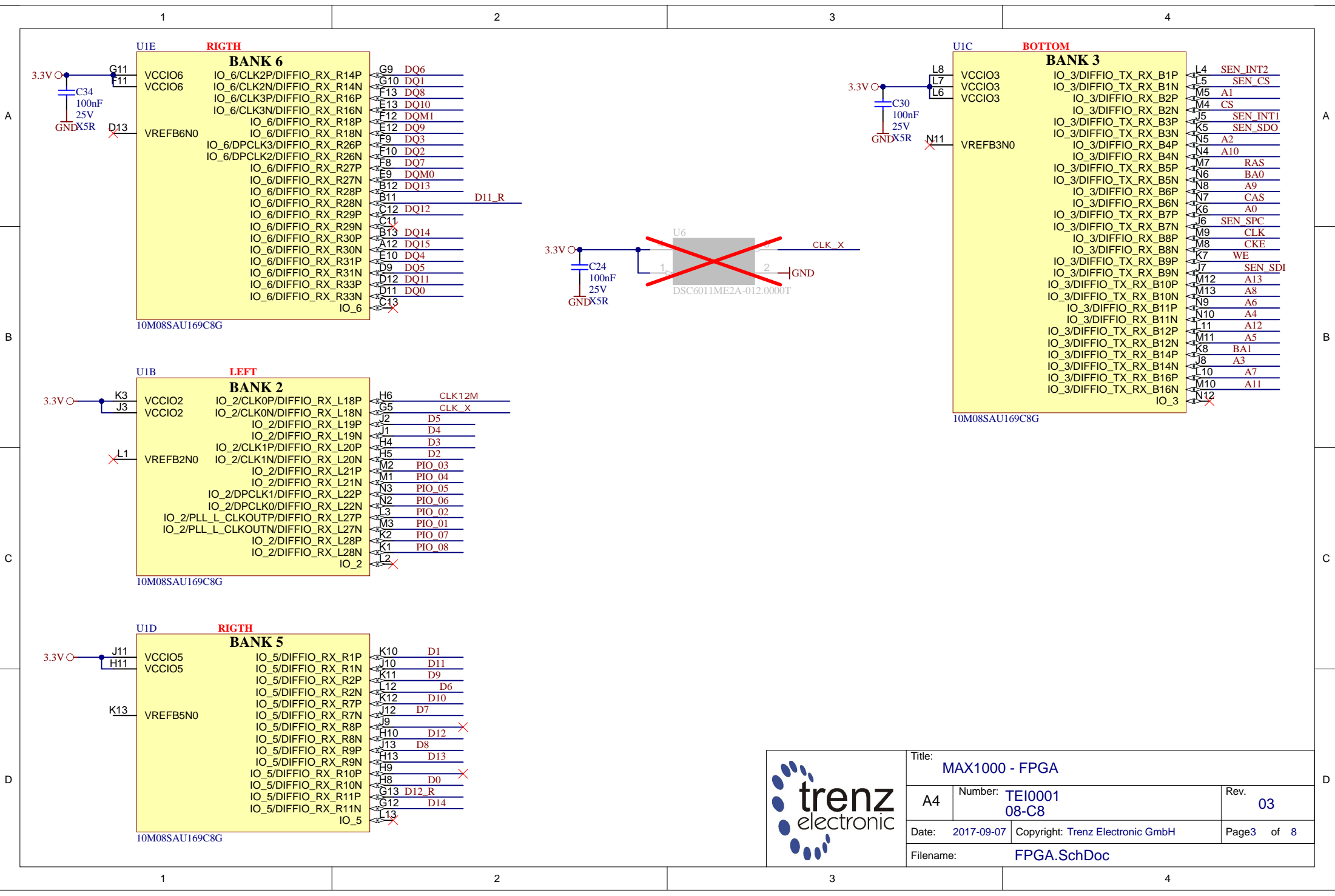
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U1E RIGTH BANK 6

VCCIO6 IO_6/CLK2P/DIFFIO_RX_R14P
VCCIO6 IO_6/CLK2N/DIFFIO_RX_R14N
IO_6/CLK3P/DIFFIO_RX_R16P
IO_6/CLK3N/DIFFIO_RX_R16N
IO_6/DIFFIO_RX_R18P
IO_6/DIFFIO_RX_R18N
IO_6/DPCLK3/DIFFIO_RX_R26P
IO_6/DPCLK2/DIFFIO_RX_R26N
IO_6/DIFFIO_RX_R27P
IO_6/DIFFIO_RX_R27N
IO_6/DIFFIO_RX_R28P
IO_6/DIFFIO_RX_R28N
IO_6/DIFFIO_RX_R29P
IO_6/DIFFIO_RX_R29N
IO_6/DIFFIO_RX_R30P
IO_6/DIFFIO_RX_R30N
IO_6/DIFFIO_RX_R31P
IO_6/DIFFIO_RX_R31N
IO_6/DIFFIO_RX_R33P
IO_6/DIFFIO_RX_R33N
IO_6

VREFB6N0

10M08SAU169C8G

U1C BOTTOM BANK 3

VCCIO3 IO_3/DIFFIO_TX_RX_B1P
VCCIO3 IO_3/DIFFIO_TX_RX_B1N
VCCIO3 IO_3/DIFFIO_RX_B2P
IO_3/DIFFIO_RX_B2N
IO_3/DIFFIO_RX_B2P
IO_3/DIFFIO_TX_RX_B3P
IO_3/DIFFIO_TX_RX_B3N
IO_3/DIFFIO_RX_B4P
IO_3/DIFFIO_RX_B4N
IO_3/DIFFIO_TX_RX_B5P
IO_3/DIFFIO_TX_RX_B5N
IO_3/DIFFIO_TX_RX_B6P
IO_3/DIFFIO_TX_RX_B6N
IO_3/DIFFIO_TX_RX_B7P
IO_3/DIFFIO_TX_RX_B7N
IO_3/DIFFIO_RX_B8P
IO_3/DIFFIO_RX_B8N
IO_3/DIFFIO_TX_RX_B9P
IO_3/DIFFIO_TX_RX_B9N
IO_3/DIFFIO_TX_RX_B10P
IO_3/DIFFIO_TX_RX_B10N
IO_3/DIFFIO_RX_B11P
IO_3/DIFFIO_RX_B11N
IO_3/DIFFIO_TX_RX_B12P
IO_3/DIFFIO_TX_RX_B12N
IO_3/DIFFIO_TX_RX_B14P
IO_3/DIFFIO_TX_RX_B14N
IO_3/DIFFIO_TX_RX_B16P
IO_3/DIFFIO_TX_RX_B16N
IO_3

VREFB3N0

10M08SAU169C8G

U1B LEFT BANK 2

VCCIO2 IO_2/CLK0P/DIFFIO_RX_L18P
VCCIO2 IO_2/CLK0N/DIFFIO_RX_L18N
IO_2/DIFFIO_RX_L19P
IO_2/DIFFIO_RX_L19N
IO_2/CLK1P/DIFFIO_RX_L20P
IO_2/CLK1N/DIFFIO_RX_L20N
IO_2/DIFFIO_RX_L21P
IO_2/DIFFIO_RX_L21N
IO_2/DIFFIO_RX_L21N
IO_2/DPCLK1/DIFFIO_RX_L22P
IO_2/DPCLK0/DIFFIO_RX_L22N
IO_2/PLL_L_CLKOUTP/DIFFIO_RX_L27P
IO_2/PLL_L_CLKOUTN/DIFFIO_RX_L27N
IO_2/DIFFIO_RX_L28P
IO_2/DIFFIO_RX_L28N
IO_2

VREFB2N0

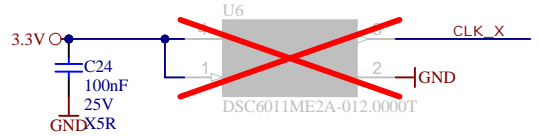
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U1D RIGTH BANK 5

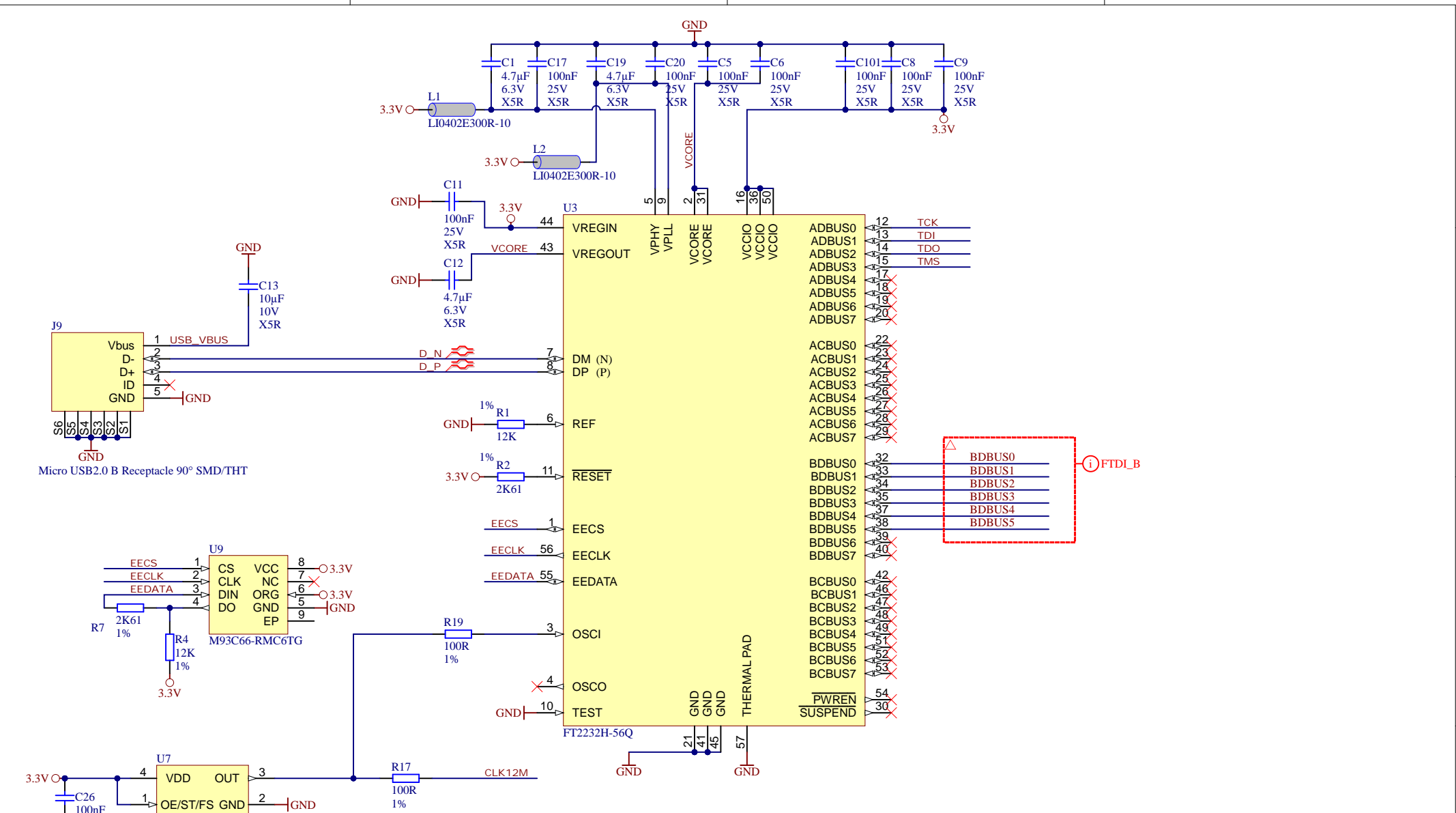
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VCCIO5 IO_5/DIFFIO_RX_R1N
IO_5/DIFFIO_RX_R2P
IO_5/DIFFIO_RX_R2N
IO_5/DIFFIO_RX_R7P
IO_5/DIFFIO_RX_R7N
IO_5/DIFFIO_RX_R7N
IO_5/DIFFIO_RX_R8P
IO_5/DIFFIO_RX_R8N
IO_5/DIFFIO_RX_R9P
IO_5/DIFFIO_RX_R9N
IO_5/DIFFIO_RX_R9N
IO_5/DIFFIO_RX_R10P
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IO_5

VREFB5N0

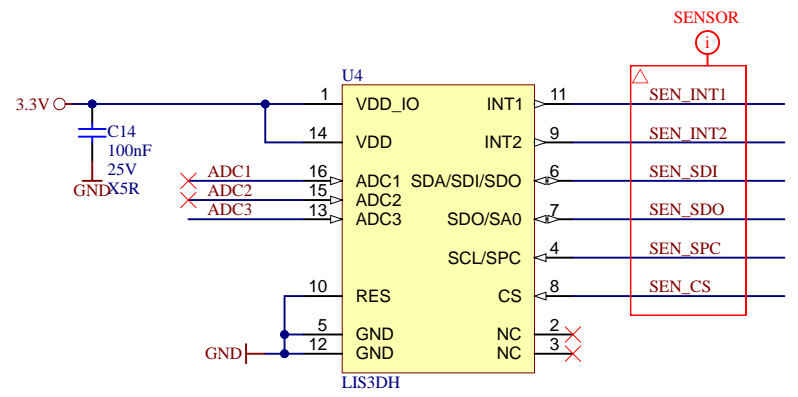
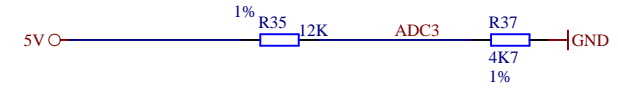
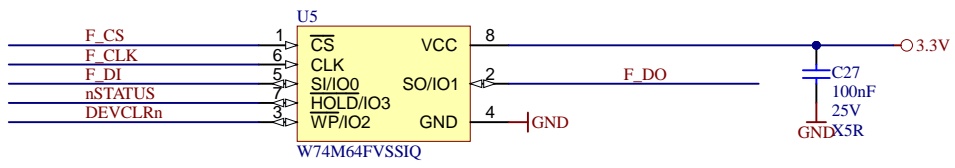
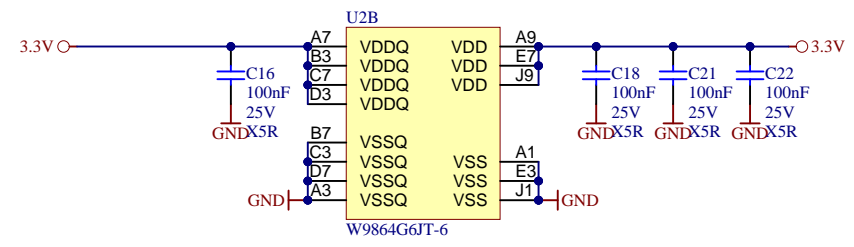
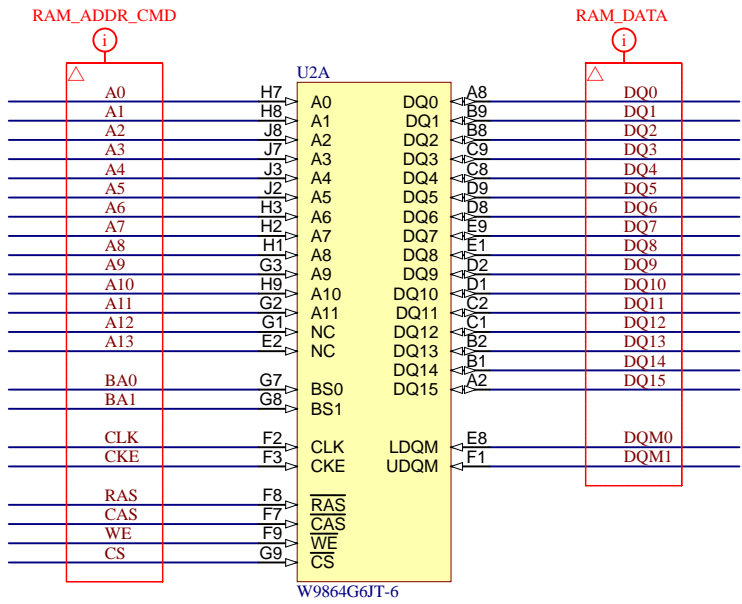
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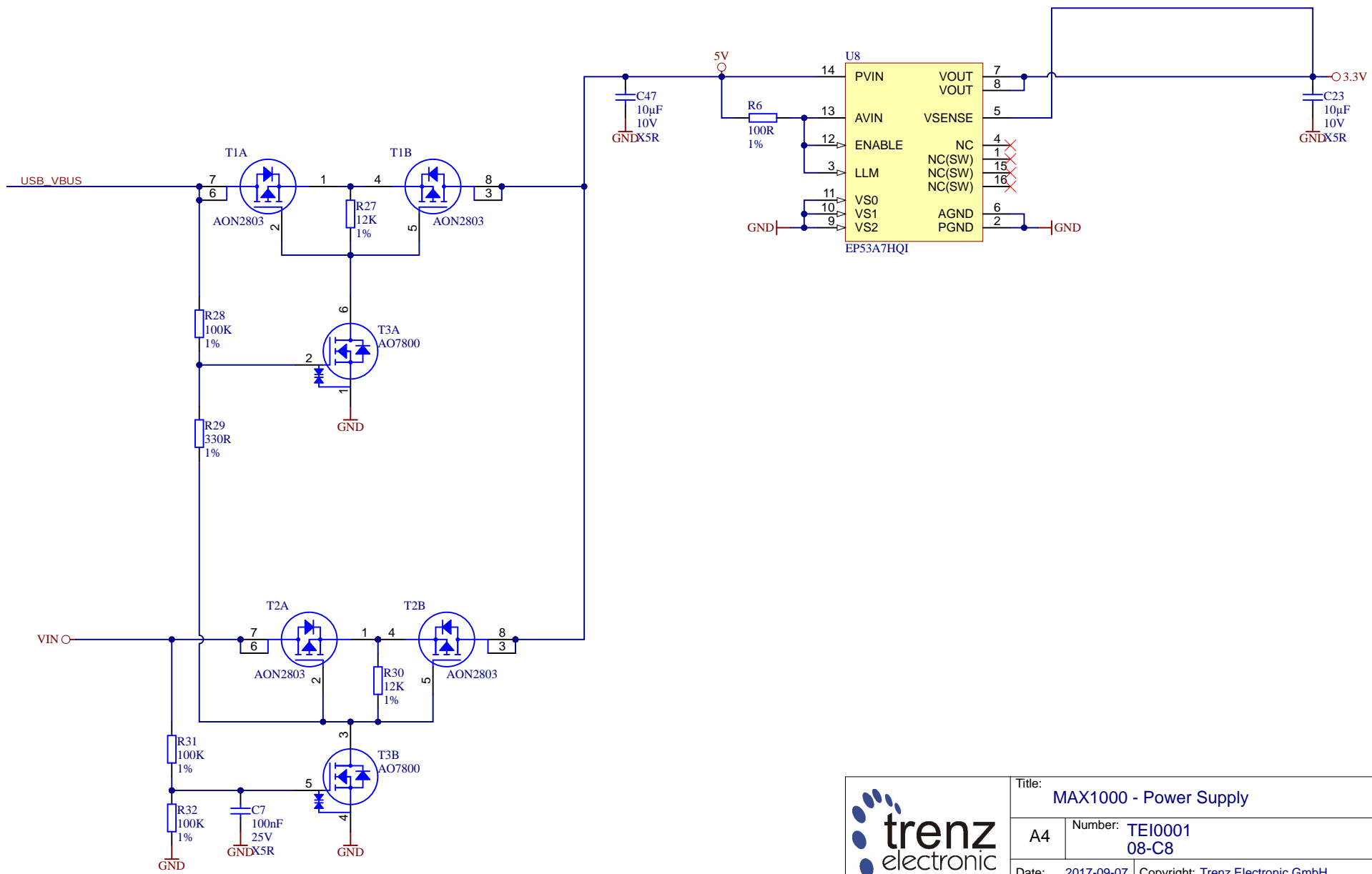
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Date: 2017-09-07	Copyright: Trenz Electronic GmbH	Page3 of 8
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Title: MAX1000 - Sensor_Flash_SDRAM		
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	Title: MAX1000 - Power Supply		
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CHANGES REV01 to REV02

- 1) Added resistors R35, R37
- 2) Changed SPI-flash on W74M64FVSSIQ (populated in REV02)
- 3) Moved nets D0,D1,D4,D5 from bank 1B to banks 5 and 2
- 4) Optimized connections into banks 1A, 1B, 2, 5,6
- 5) Changed pin-out on a connector J4
- 6) Added a pin-header J3
- 7) Changed a LEDs

CHANGES REV02a (29.08.2017)

- 1) Updated schematic symbol of U1

CHANGES REV03

- 1) Buttons S1 and S2 replaced from KMS221GLFS to KMS221GPLFS

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	Title: MAX1000		
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