

1

2

3

4

A

A

B

B

C

C

D

D

U_A-Headers
A-Headers.SchDoc



U_FPGA
FPGA.SchDoc



U_ADC
ADC.SchDoc



U_FTDI
FTDI.SchDoc



U_SENSOR_Flash_RAM
SENSOR_Flash_RAM.SchDoc



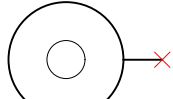
U_FPGA_MISC
FPGA_MISC.SchDoc



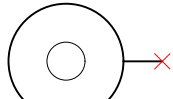
U_PowerSupply
PowerSupply.SchDoc



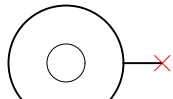
U_A_POWER
Analog_power.SchDoc



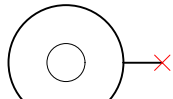
Mount.Hole 2mm (unplated)



Mount.Hole 2mm (unplated)

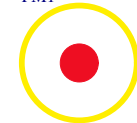


Mount.Hole 2mm (unplated)



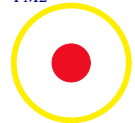
Mount.Hole 2mm (unplated)

PM1



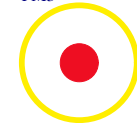
FIDU-DOT - mini

PM2



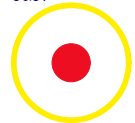
FIDU-DOT - mini

PM3



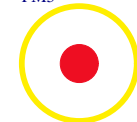
FIDU-DOT - mini

PM4



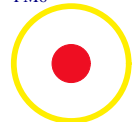
FIDU-DOT - mini

PM5



FIDU-DOT - mini

PM6



FIDU-DOT - mini

Assembly variant	08-C8A
Created by	IG
Modified by	IG
Modified at	2019-07-16
SVN Revision	11145

Serial
Serial
Serialnumber 6,3 x 6.3mm



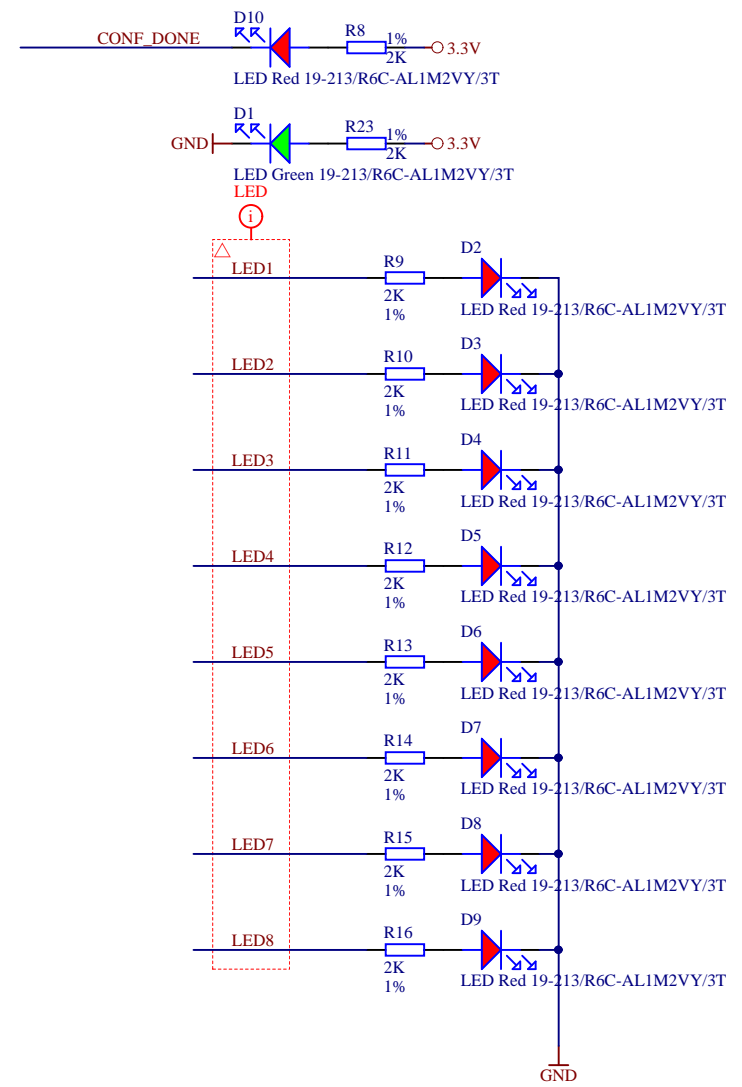
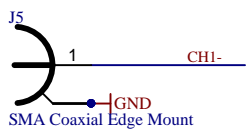
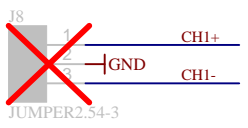
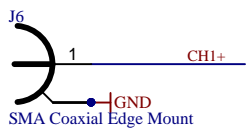
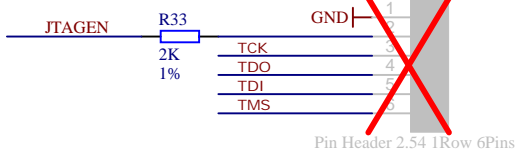
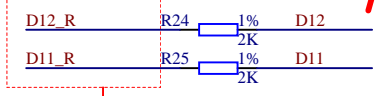
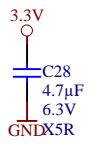
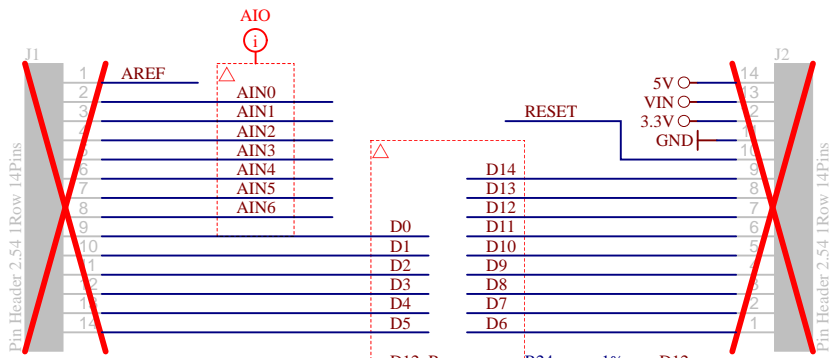
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Date: 2019-07-16	Copyright: Trenz Electronic GmbH	Page 1 of 10
Drawn by: VY	Filename: TEI0015.SchDoc	

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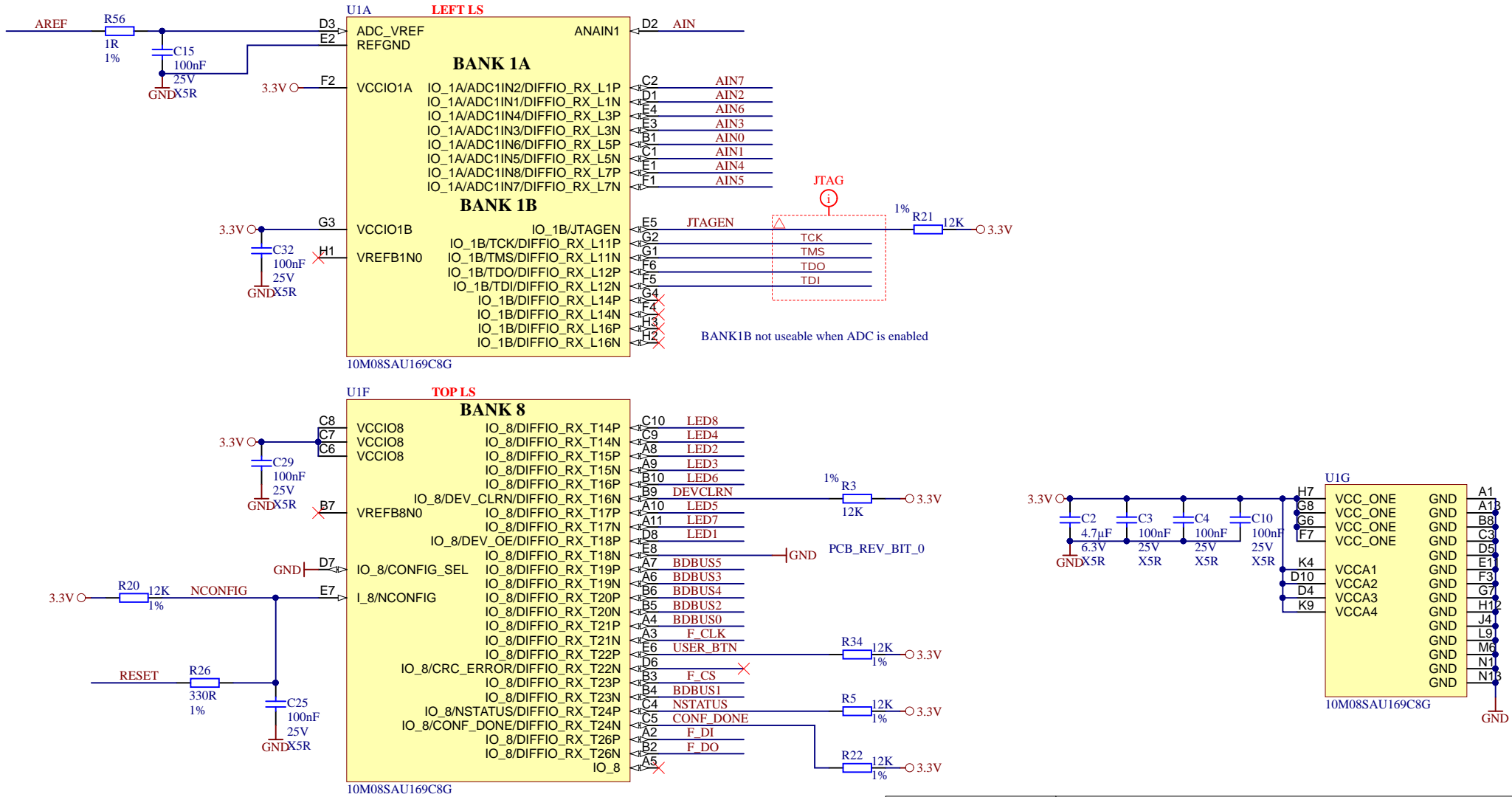
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Date: 2019-07-16	Copyright: Trenz Electronic GmbH	Page 2 of 10
Drawn by: VY	Filename: A-Headers.SchDoc	



	Title: FPGA MISC	
	A4	Number: TEI0015 08-C8A
	Date: 2019-07-16	Copyright: Trenz Electronic GmbH
	Drawn by: VY	Filename: FPGA_MISC.SchDoc
		Rev. 02
		Page 3 of 10

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A

A

B

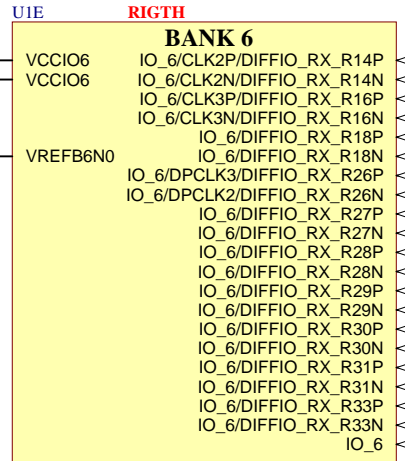
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C

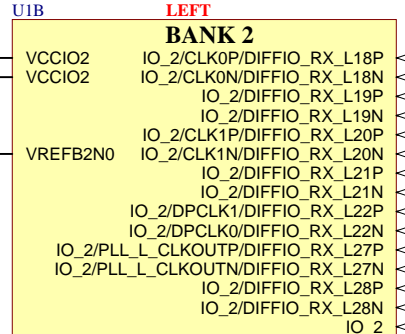
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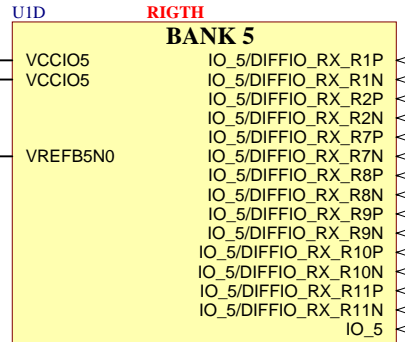
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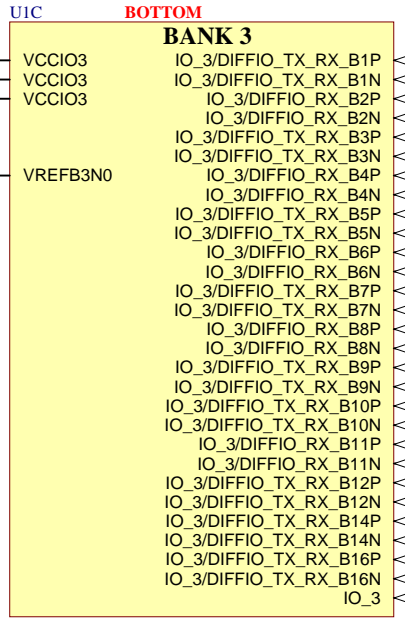
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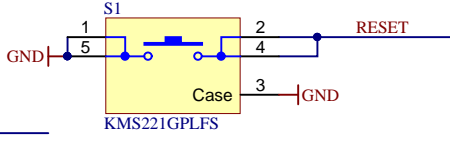
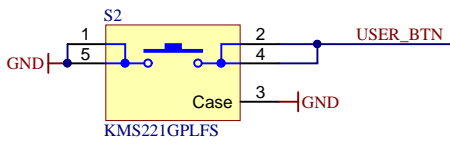
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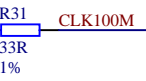
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10M08SAU169C8G



D11_R



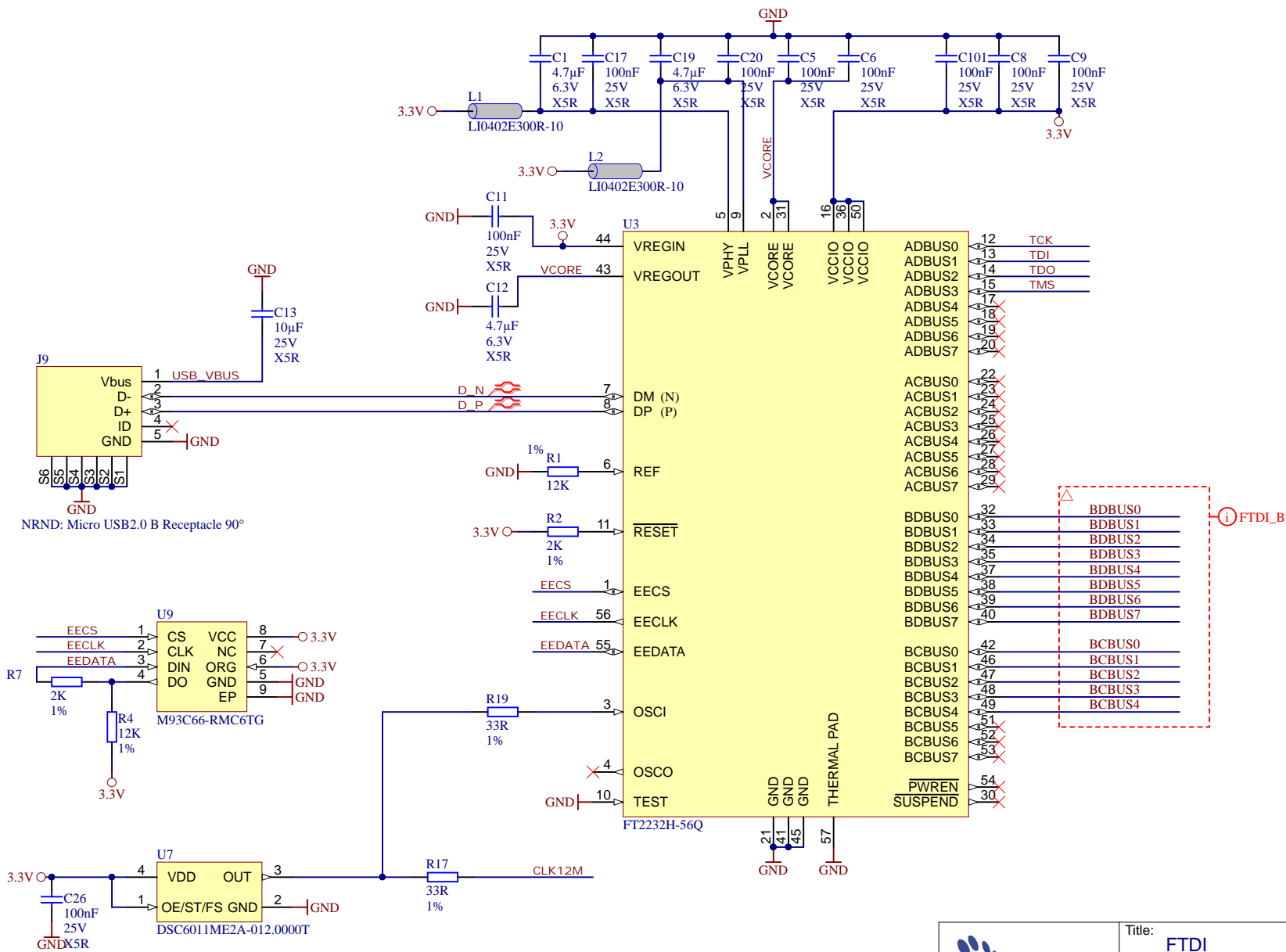
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Date: 2019-07-16	Copyright: Trenz Electronic GmbH	Page 4 of 10
Drawn by: VY	Filename: FPGA.SchDoc	


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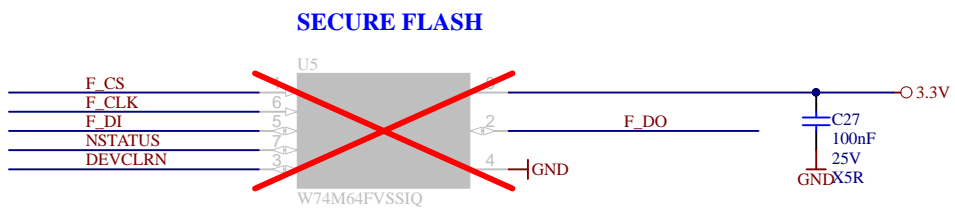
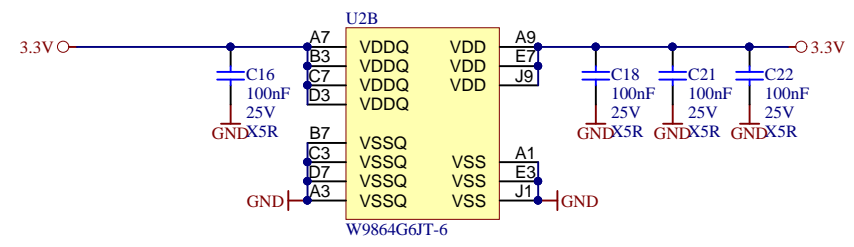
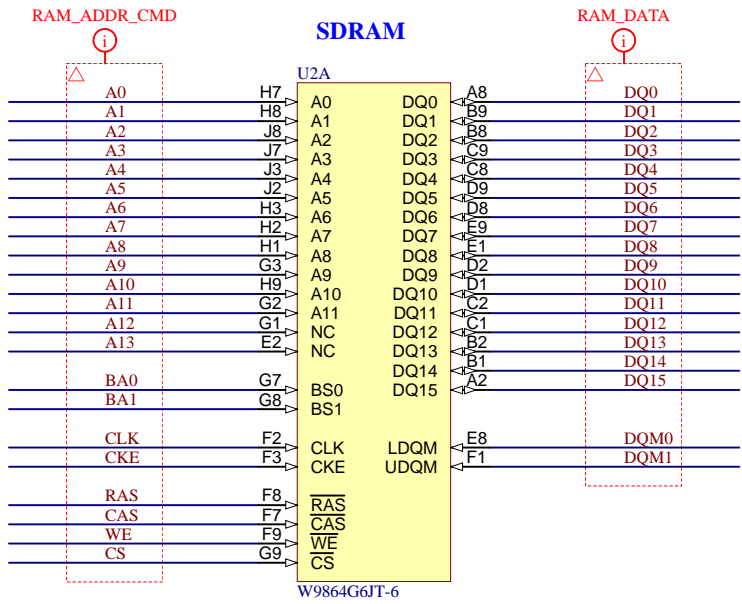
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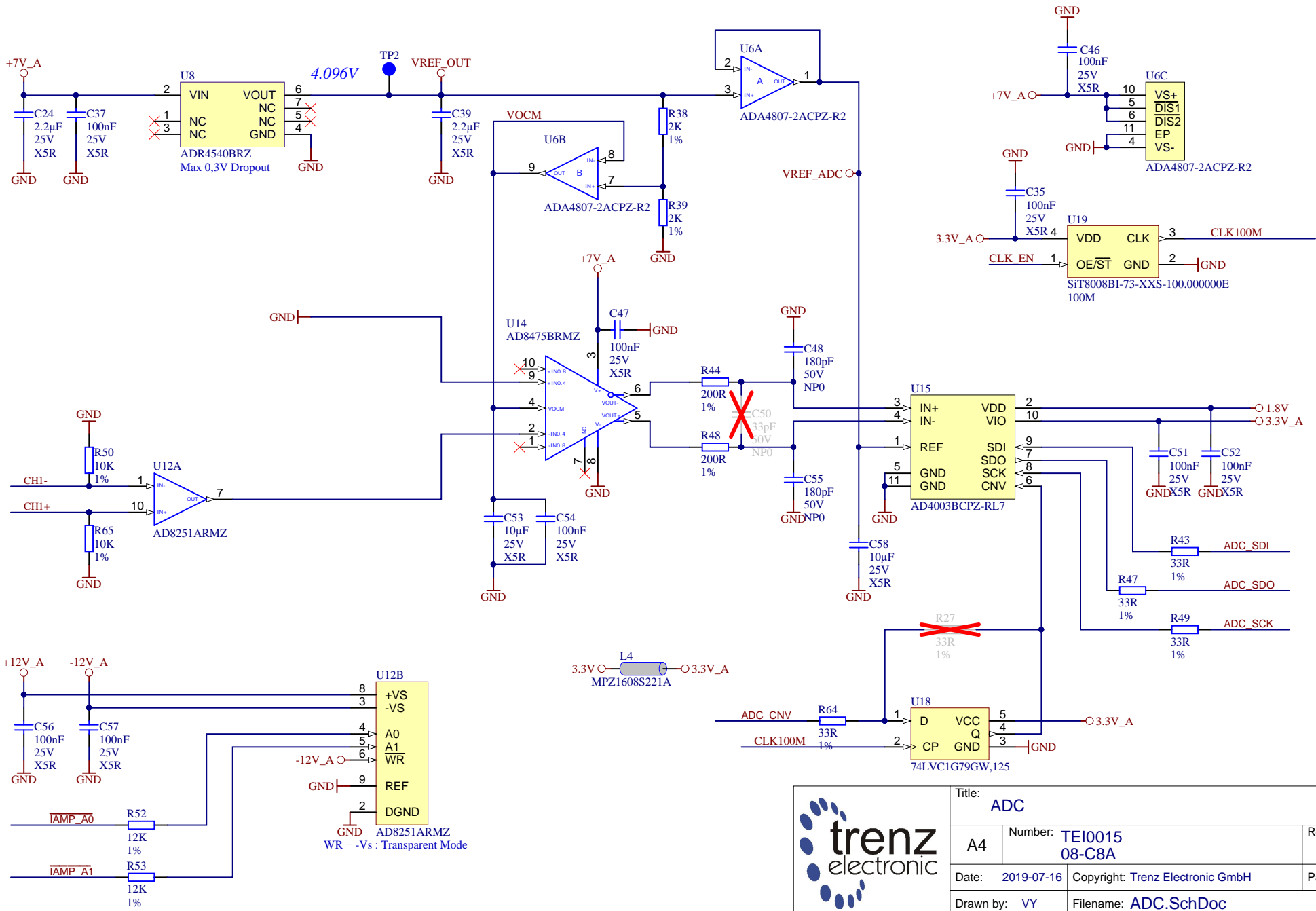
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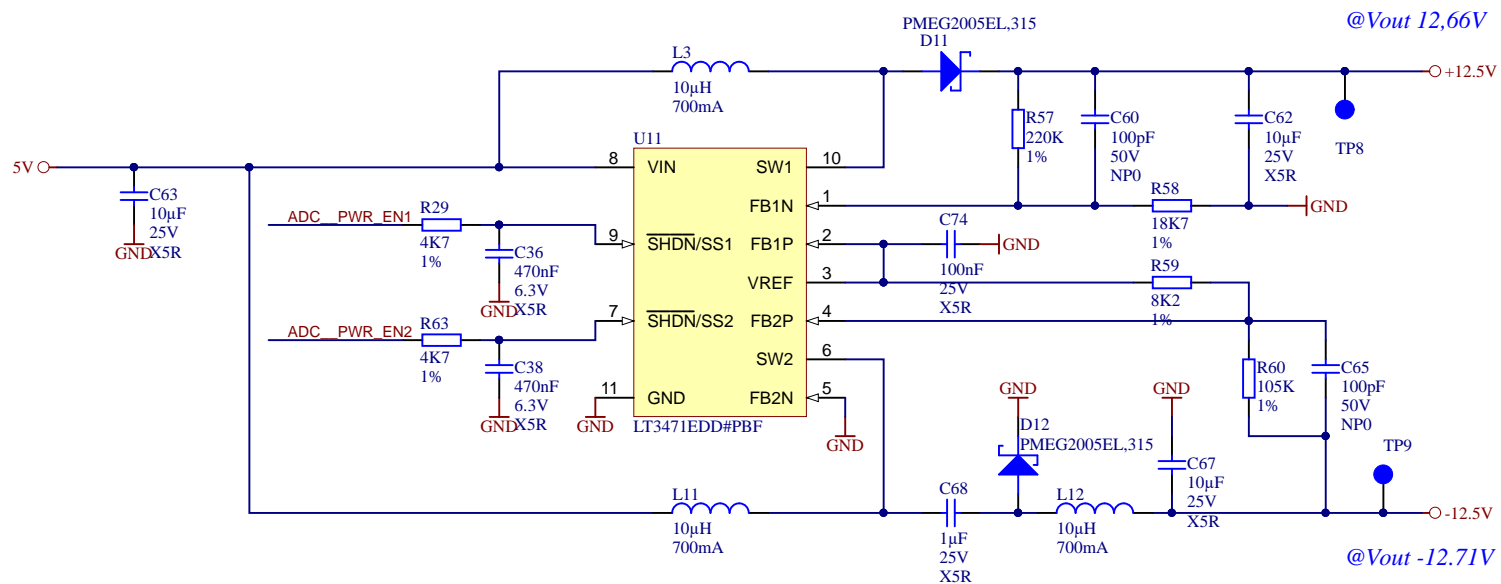
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Date: 2019-07-16		Copyright: Trenz Electronic GmbH	
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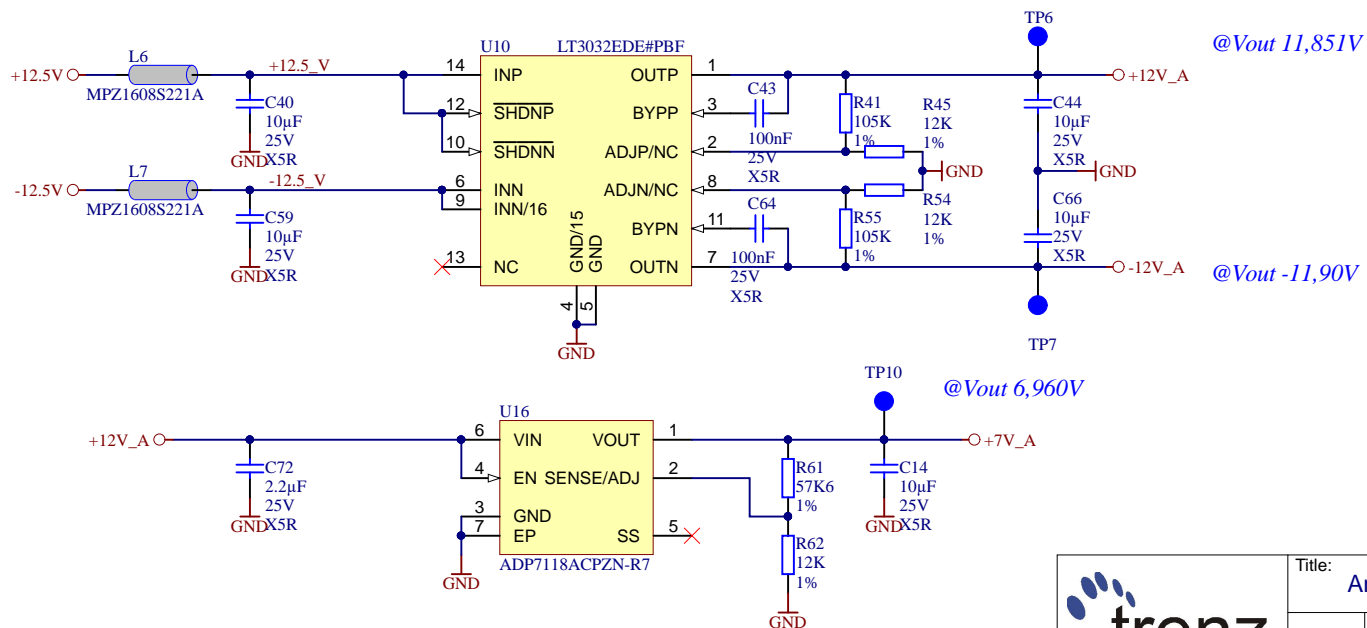
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	Date: 2019-07-16	Copyright: Trenz Electronic GmbH	Page 6 of 10
	Drawn by: VY	Filename: SENSOR_Flash_RAM.SchDoc	



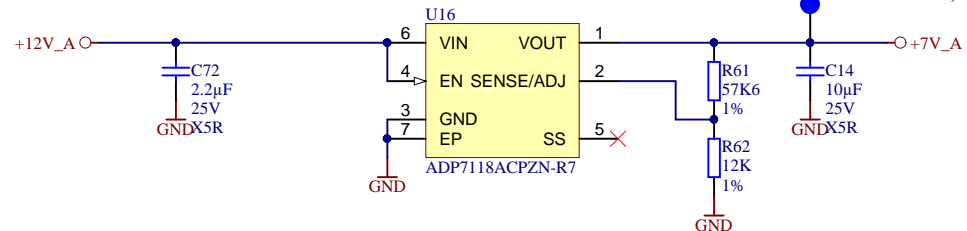
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Page 7 of 10	Page 7 of 10	
Drawn by: VY	Filename: ADC.SchDoc	



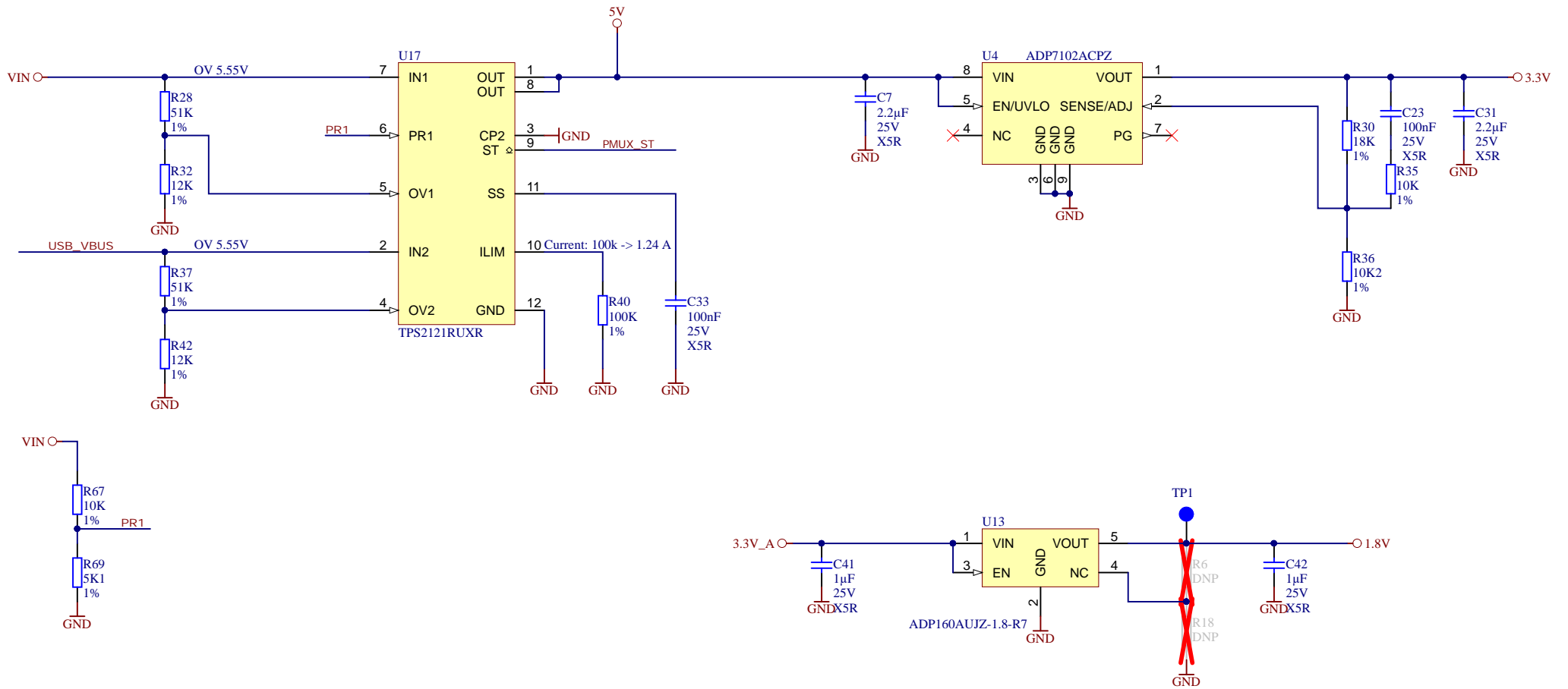
DCDC




LDO



Title: Analog power		
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Date: 2019-07-16	Copyright: Trenz Electronic GmbH	Page 8 of 10
Drawn by: VY	Filename: Analog_power.SchDoc	



		Title: PowerSupply	
		A4	Number: TEI0015 08-C8A
Date: 2019-07-16		Copyright: Trenz Electronic GmbH	
Page 9 of 10		Page 9 of 10	
Drawn by: VY		Filename: PowerSupply.SchDoc	

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Revision 02:

- 1) VY: Replaced inductors L4, L5, L8 to 10µH L0603B100MDWFT
- 2) VY: Replaced diodes D11, D12 to PMEG2005EL,315
- 3) VY: Net ADC_EN renamed to ADC_PWR_EN
- 4) ED: Deleted T1A, T1B, T2A, T2B, T3A, T3B, R27, R28, R29, R30, R31, R32, R40 and C7
- 5) ED: Inserted power mux (U17) instead
- 6) ED: Changed U4 to LDO (ADP7102ACPZ)
- 7) ED: Inserted D-Flipflop (U18)
- 8) ED: Inserted oscillator (U19)
- 9) ED: Replaced inductors L3, L11, L12 to 10 µH MLP2520S100STOS1

A

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B


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C

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D

D

	Title: Revision Changes		
	A4	Number: TEI0015 08-C8A	Rev. 02
	Date: 2019-07-01	Copyright: Trenz Electronic GmbH	Page 10 of 10
	Filename: Revision_Changes.SchDoc		

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4