Regarding the usage of our schematics and alike documentation for Trenz module Project.

Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified. Project is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.

Schematics and other handouts serve for informational purposes only!
CHANGES REV01 to REV02

1) Fixed I2C connection, added I2C MUX (U16)
2) Added optional pin-header connector for select JTAG boot mode
3) Added 2x user LEDs
4) Changed obsolete DDR3 chip
5) Fixed wrong type connector for CSI
6) Full update lib