U_B12
B12.SchDoc

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U_FPGA-MGT
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U_FPGA-CFG
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U_FPGA-PWR
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U_PWR1
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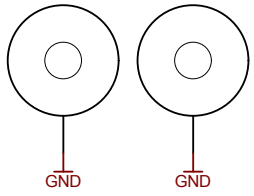
U_PWR2
PWR2.SchDoc

U_Clock
Clock.SchDoc

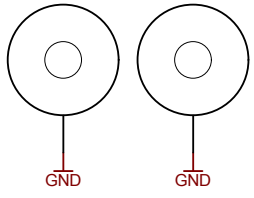
U_B2B-Connectors
B2B-Connectors.SchDoc

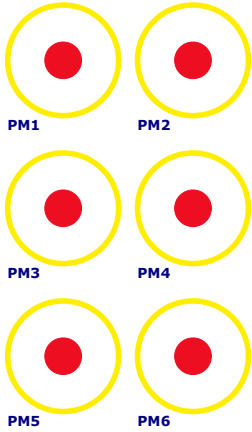
U_B33
B33.SchDoc

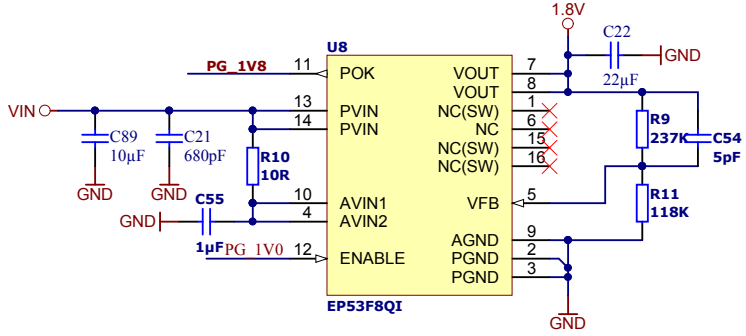
Mount.Hole 3.2mm Mount.Hole 3.2mm

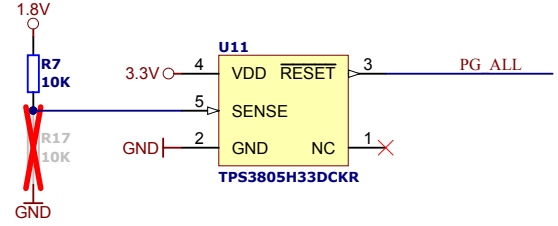


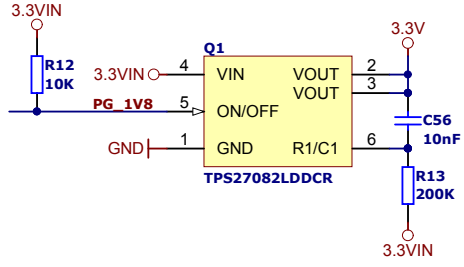
Mount.Hole 3.2mm Mount.Hole 3.2mm











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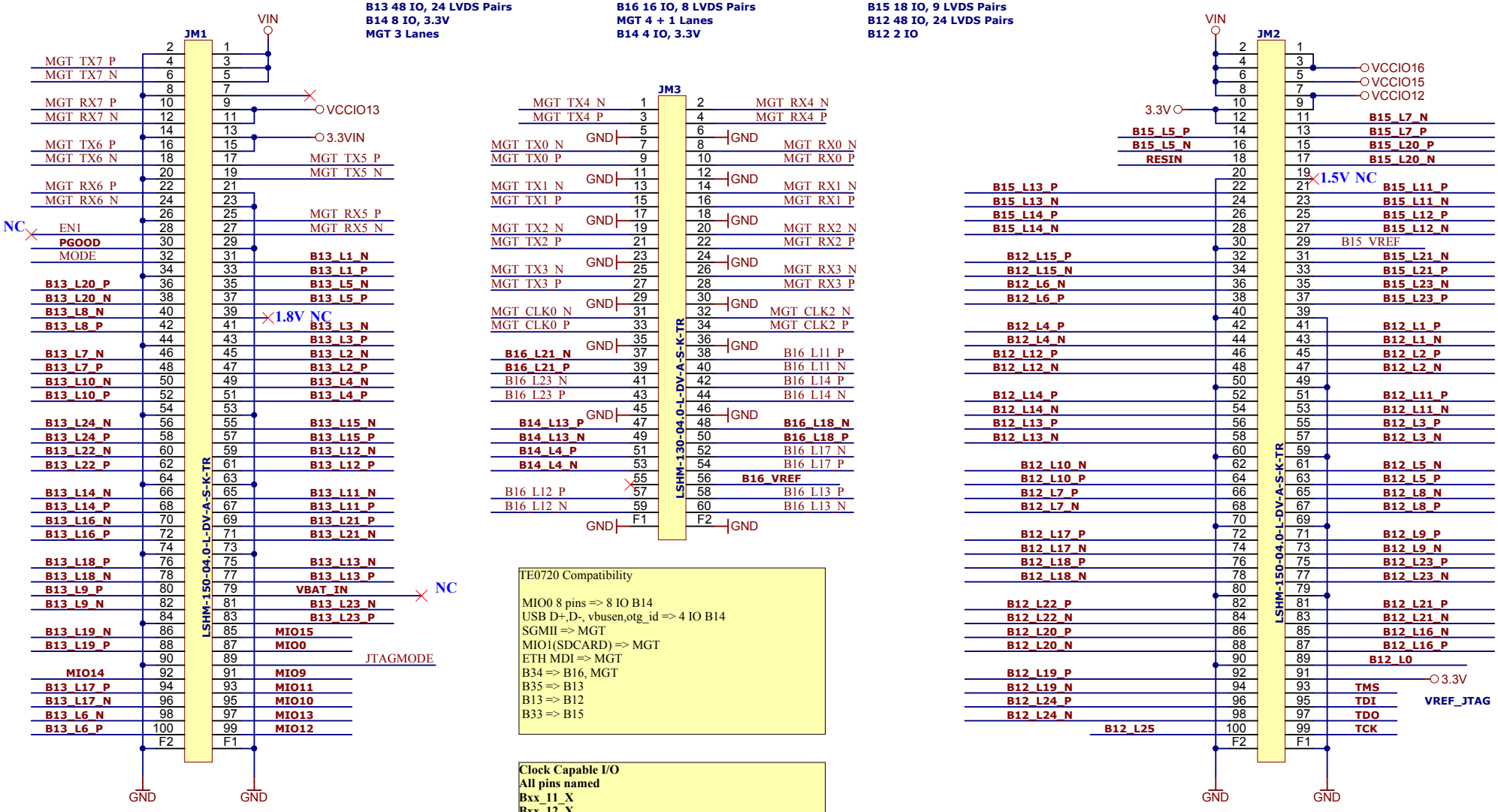

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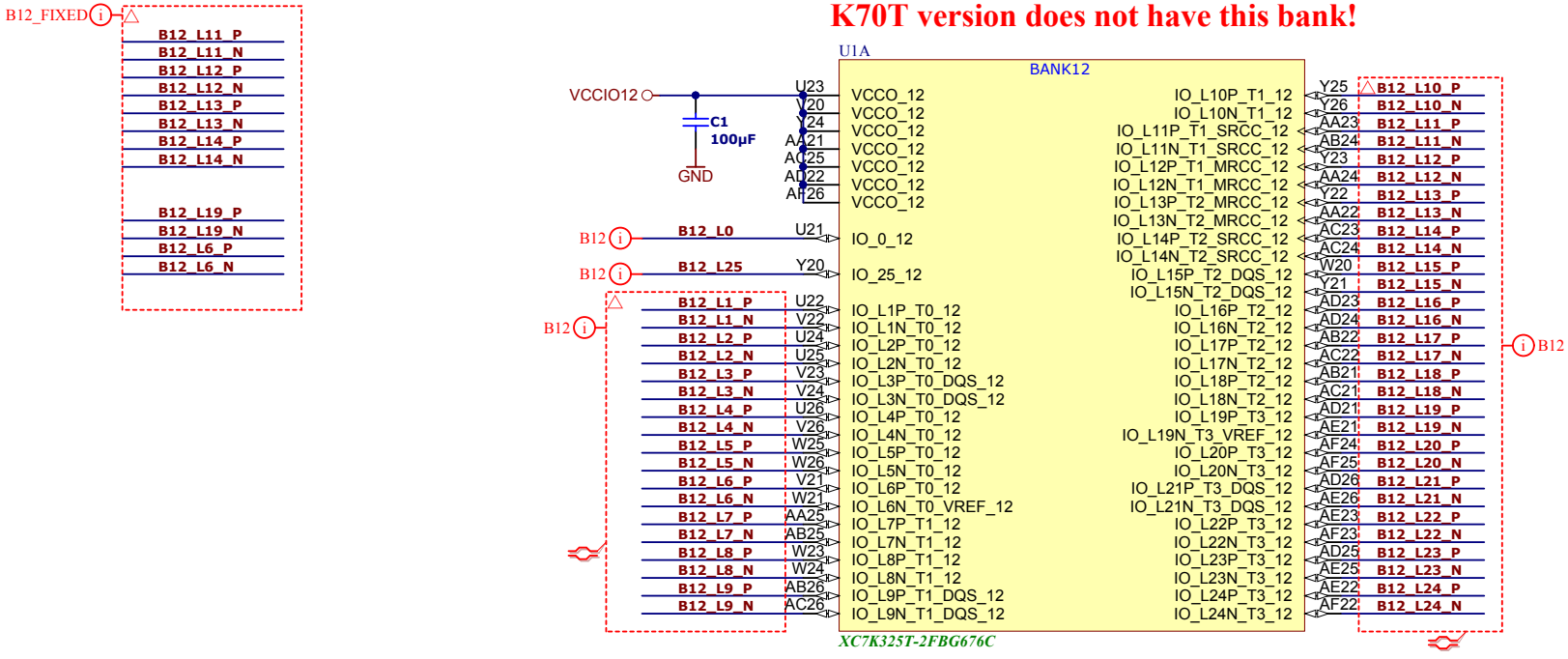

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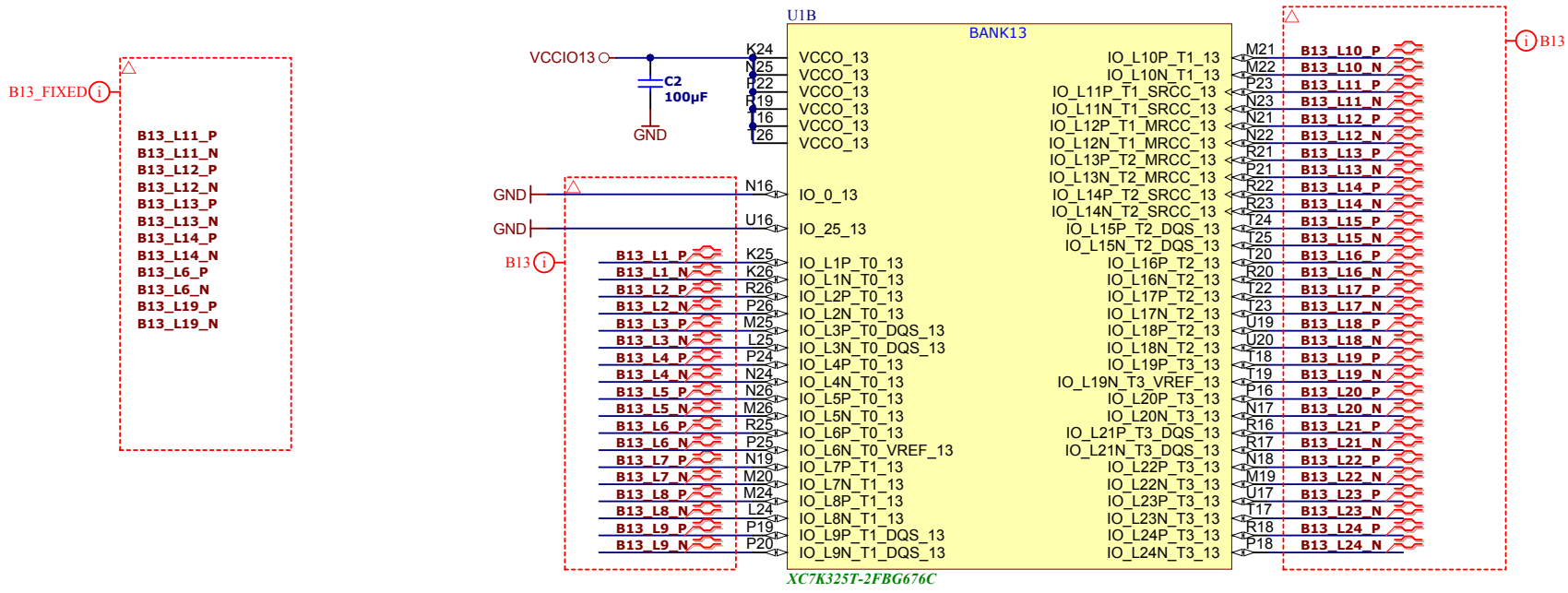

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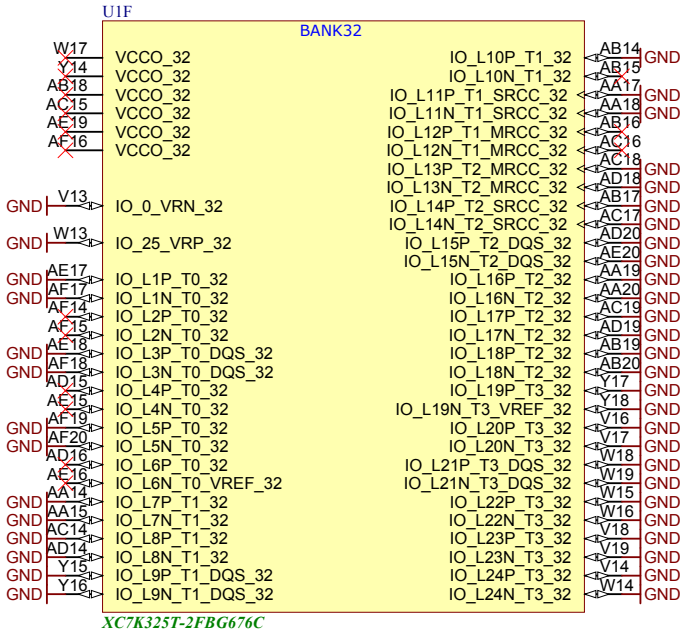

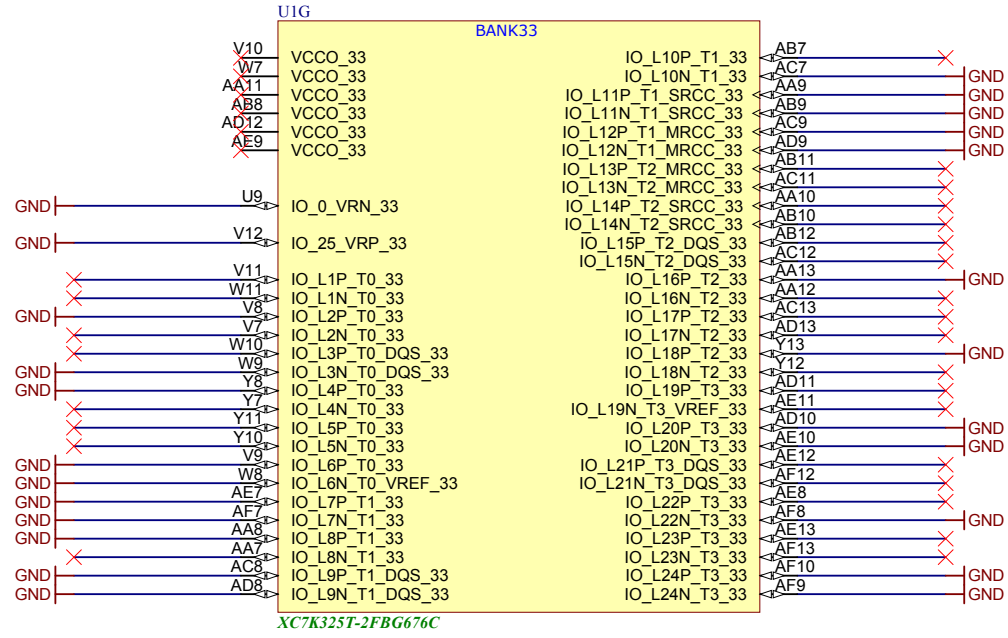

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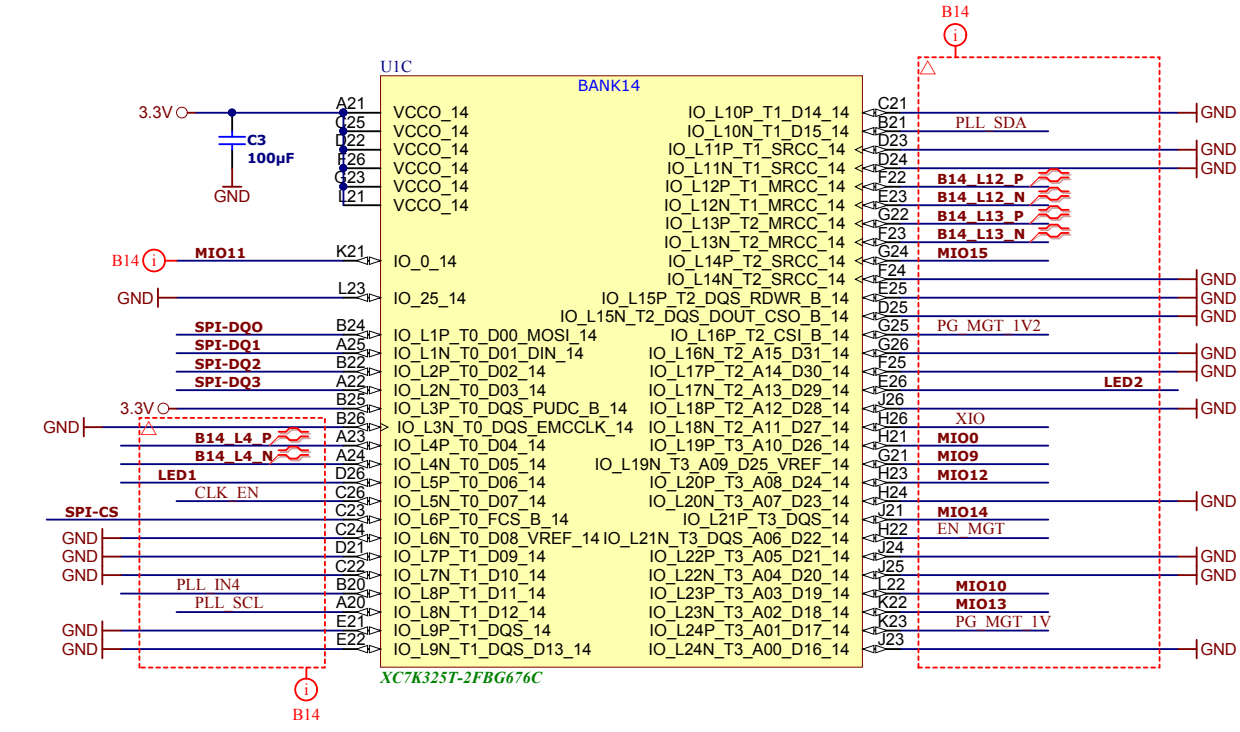

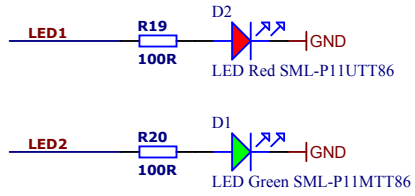

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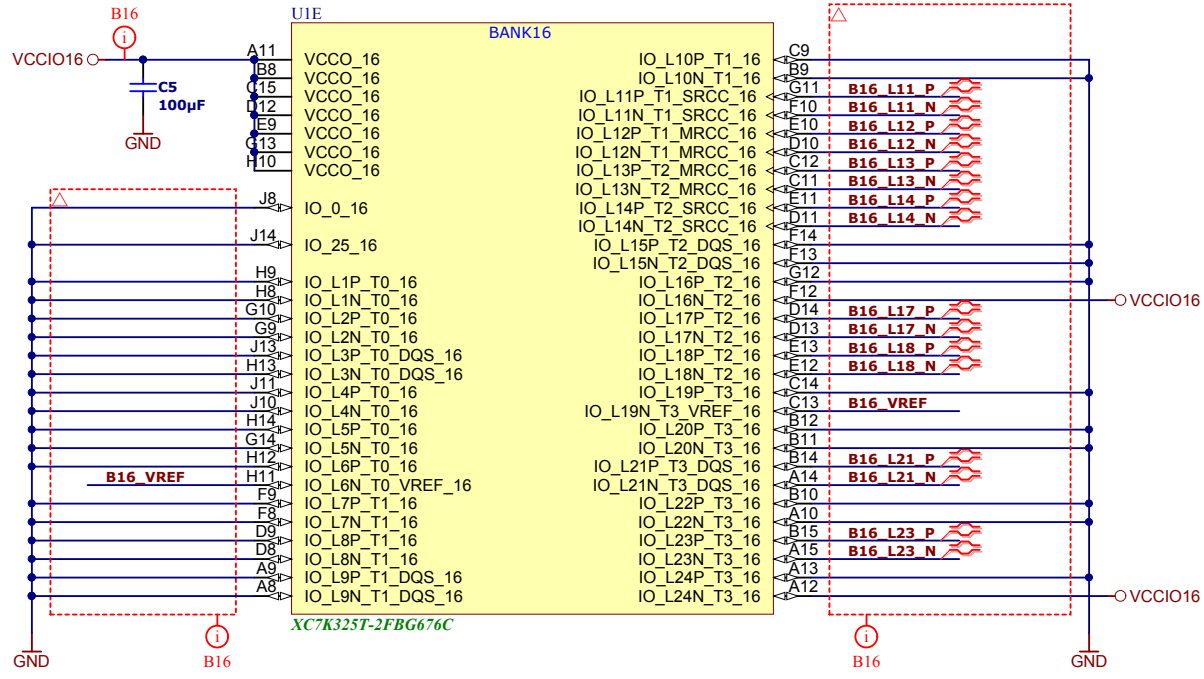

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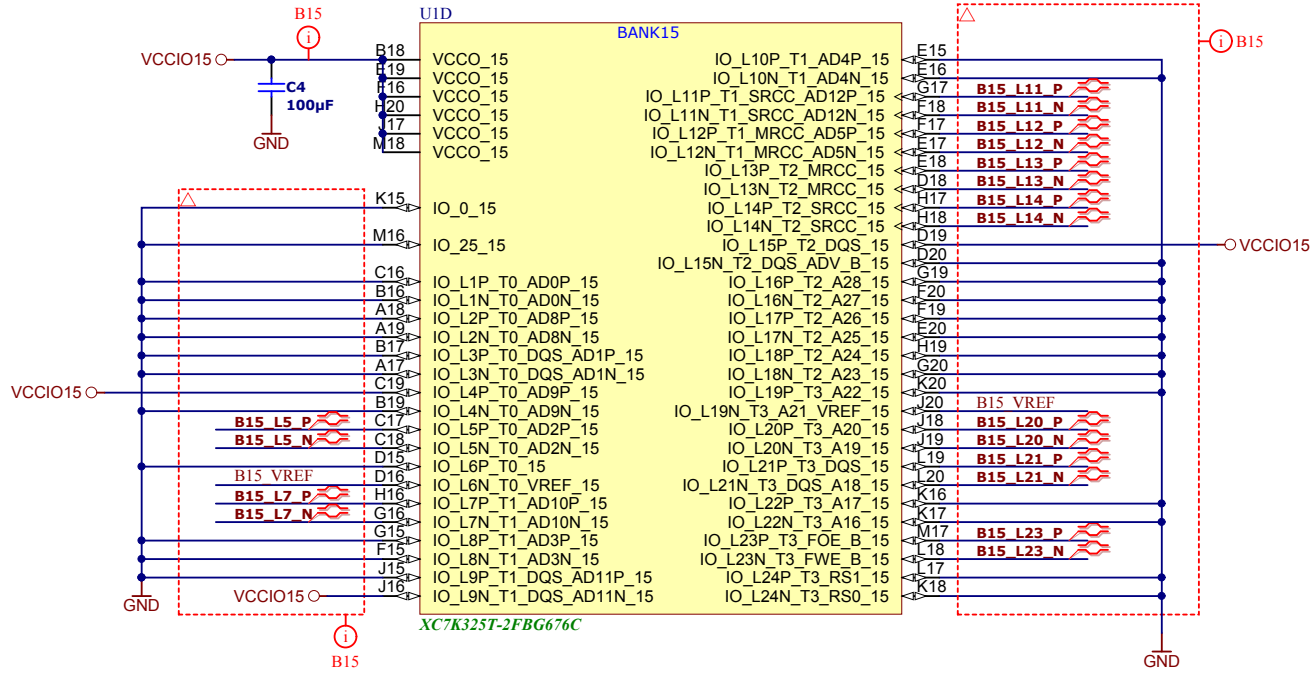

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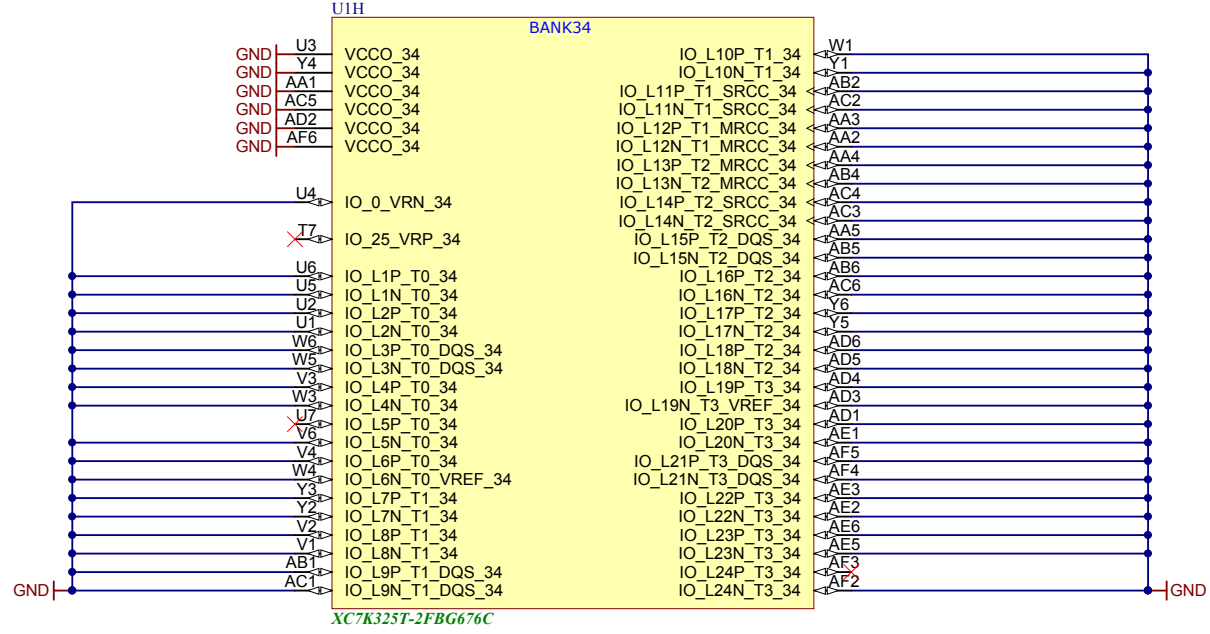

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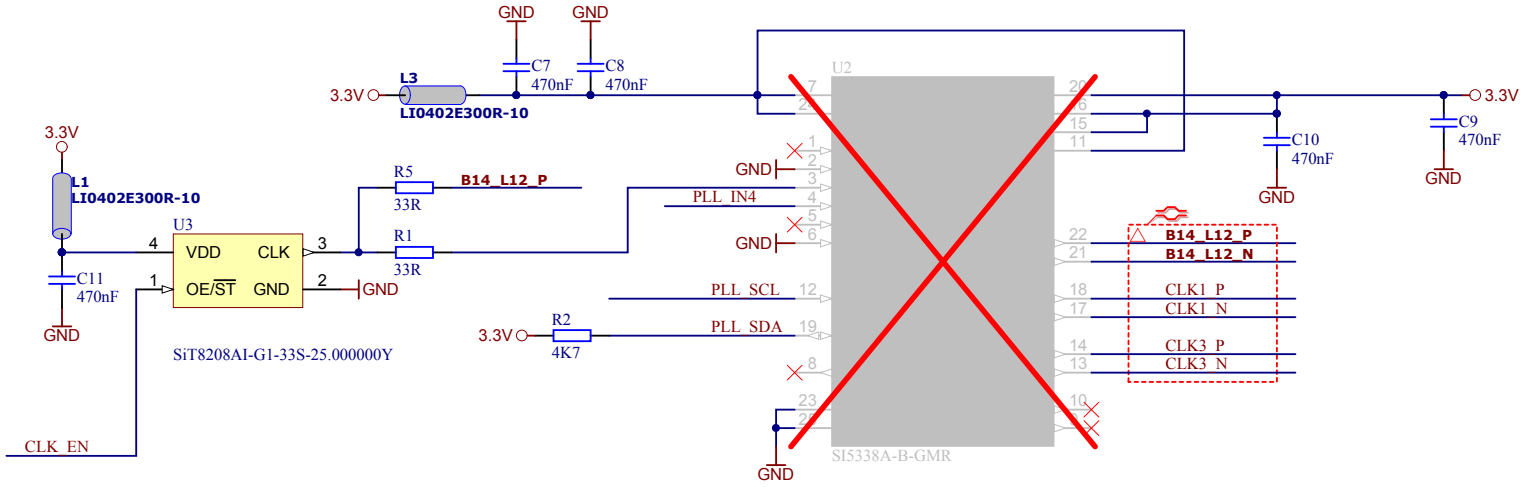

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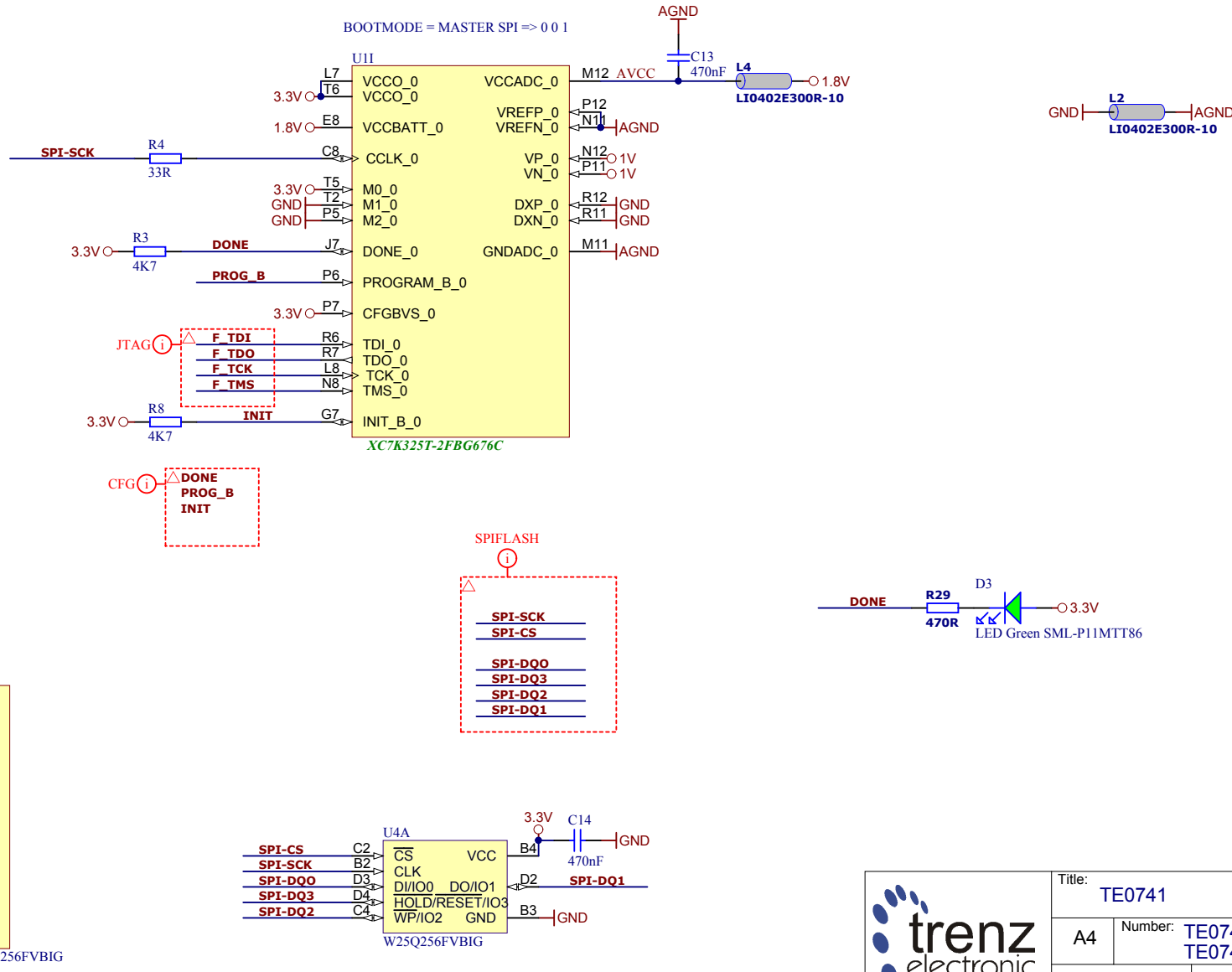

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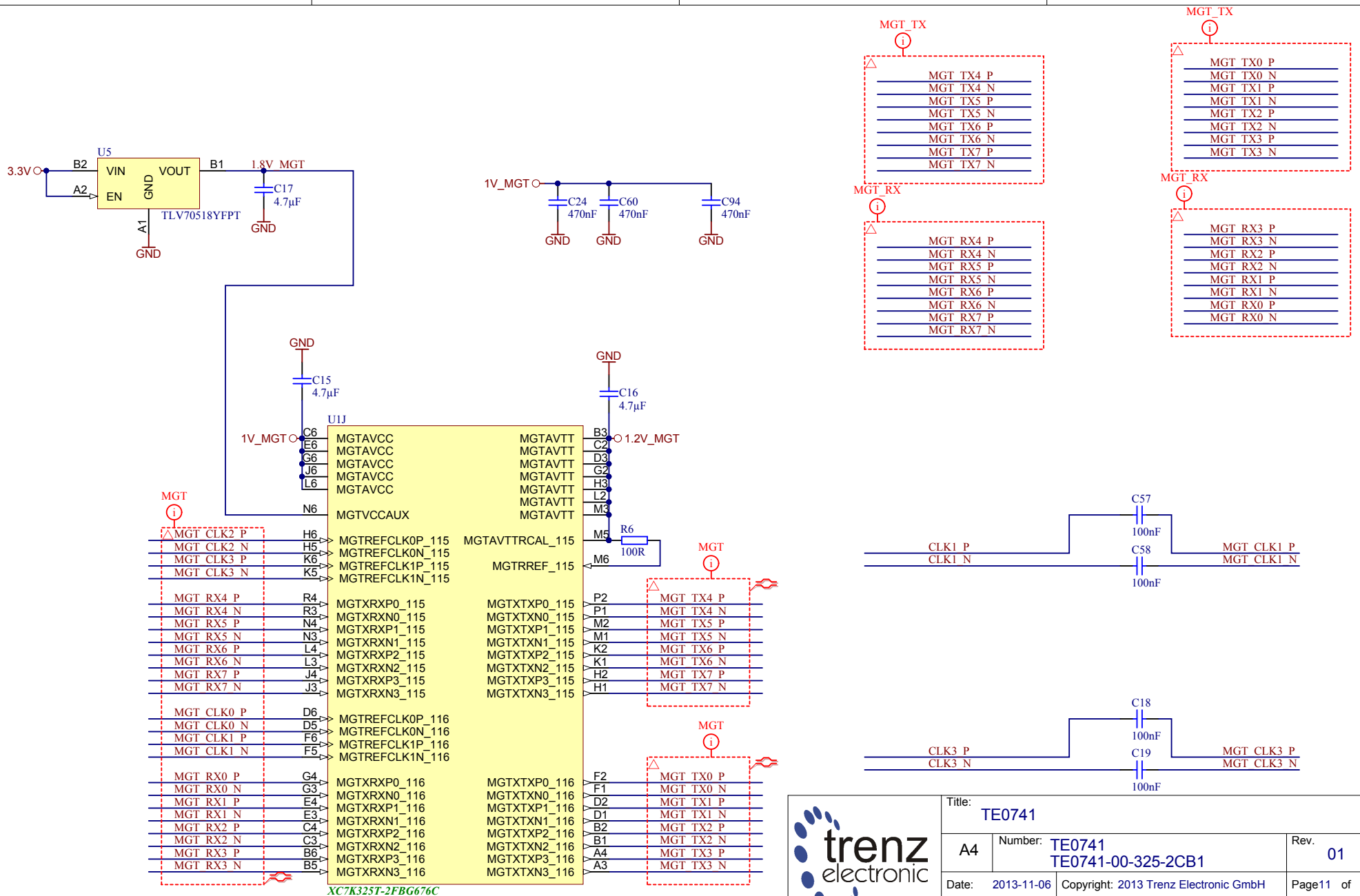
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EN
GND
VOUT
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C17
4.7µF
GND
1V_MGT
C24
470nF
C60
470nF
C94
470nF
C15
4.7µF
C16
4.7µF
U1J
MGTAVCC
MGTVCCAUX
MGTAVTT
MGTAVTTRCAL_115
MGTRREF_115
1.2V_MGT
R6
100R
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MGTREFCLK0N_115
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MGT_TX2_P
MGT_TX2_N
MGT_TX3_P
MGT_TX3_N
MGT_TX
MGT_RX
C57
100nF
C58
100nF
CLK1_P
CLK1_N
C18
100nF
C19
100nF
CLK3_P
CLK3_N
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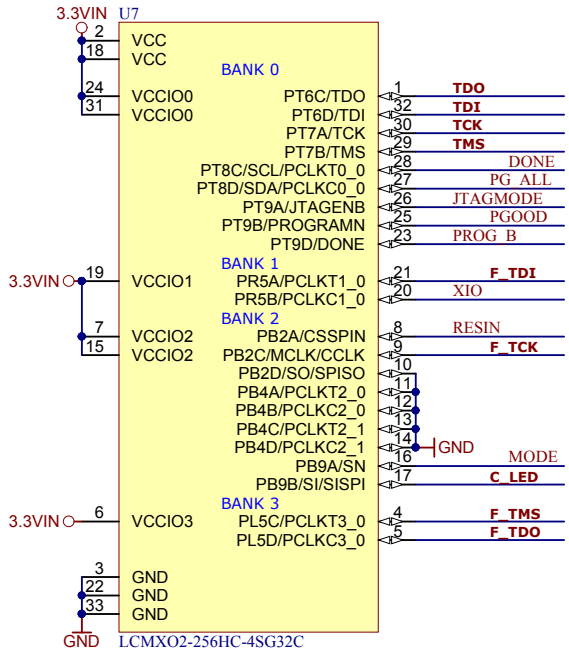
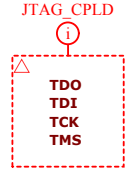
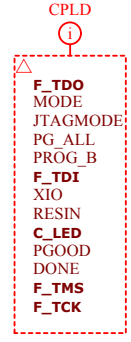
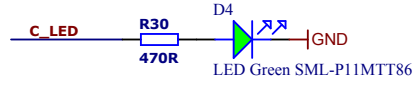
Filename: FPGA-MGT.SchDoc









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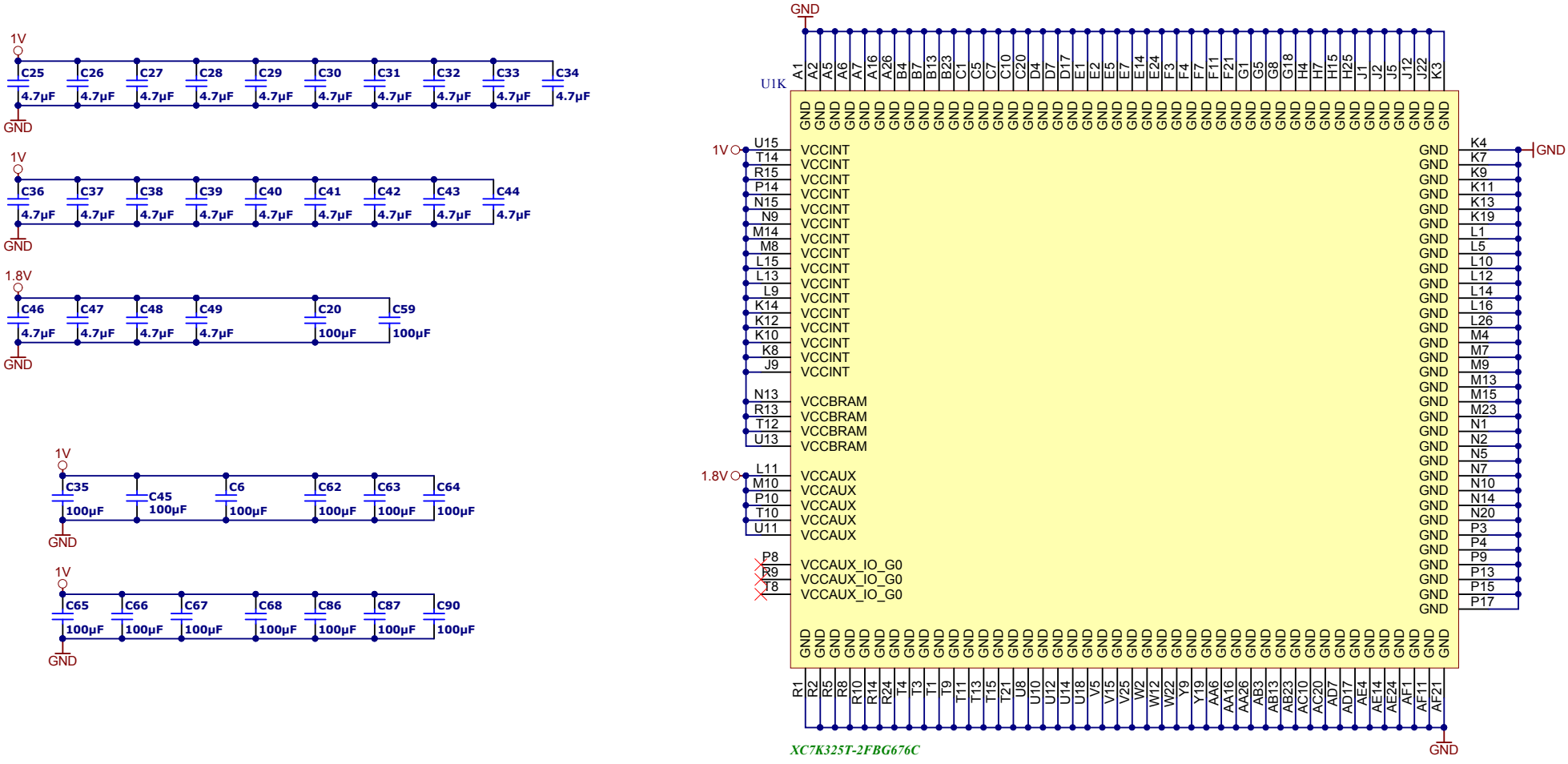

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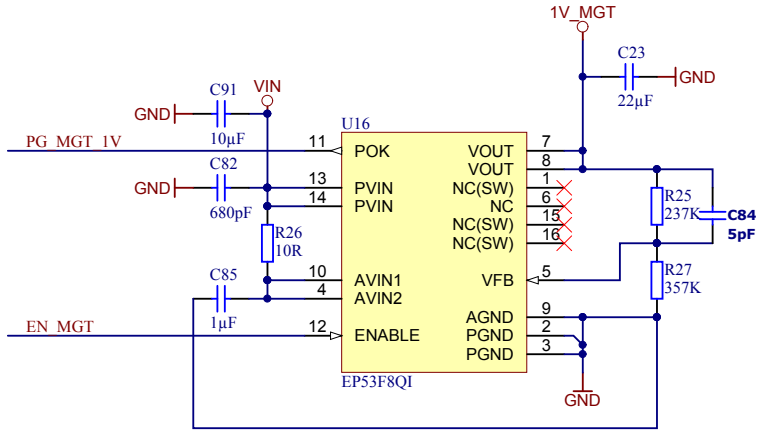

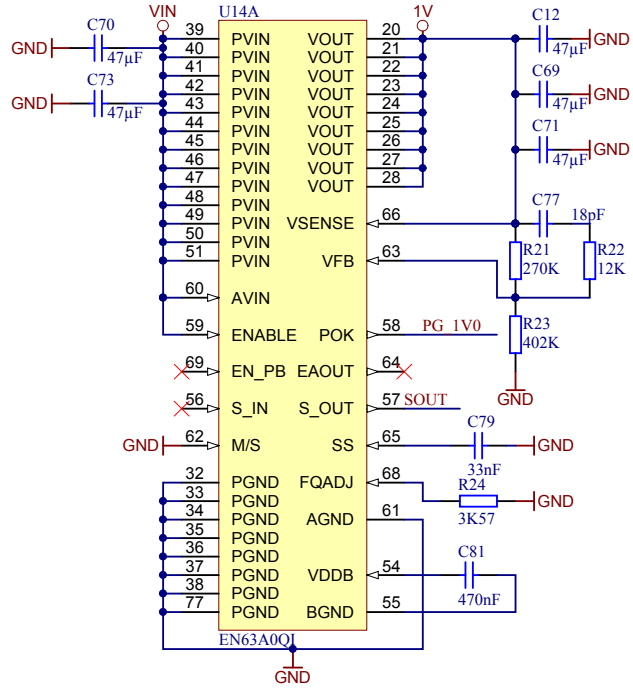

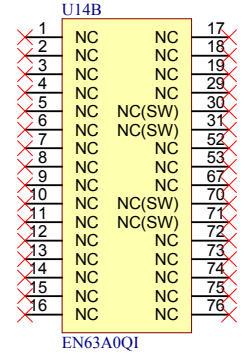

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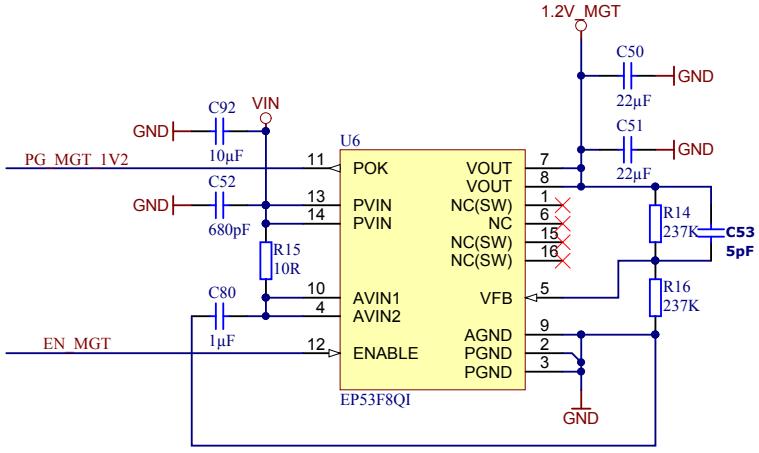

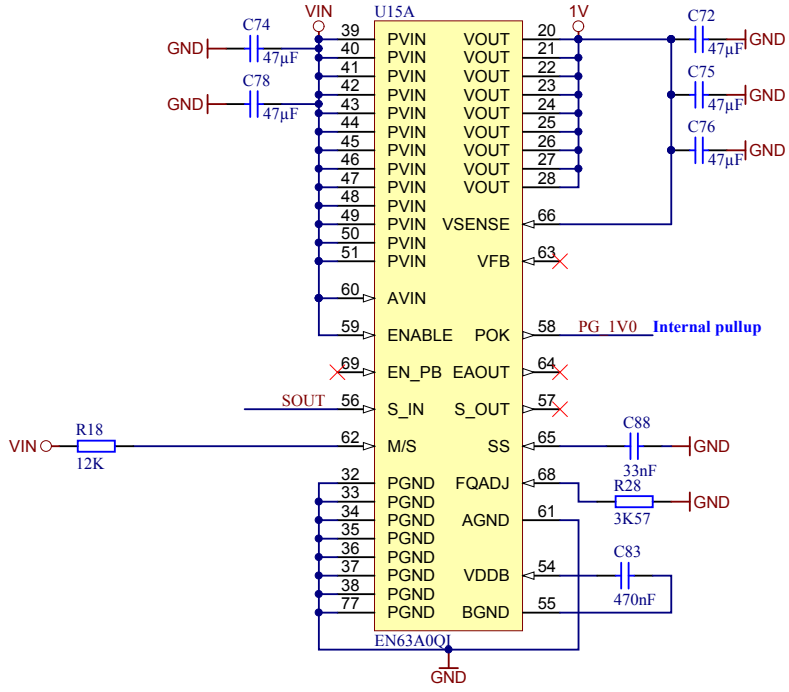

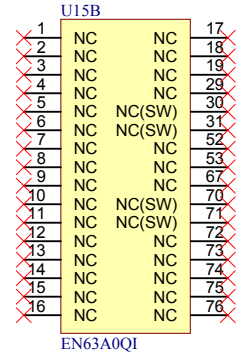

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