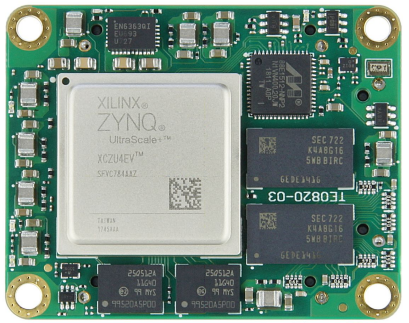
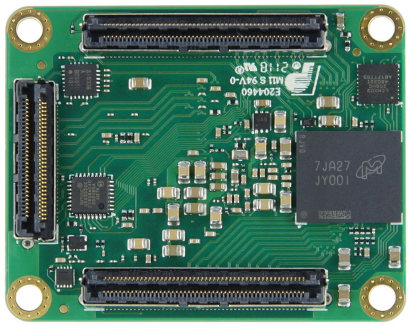


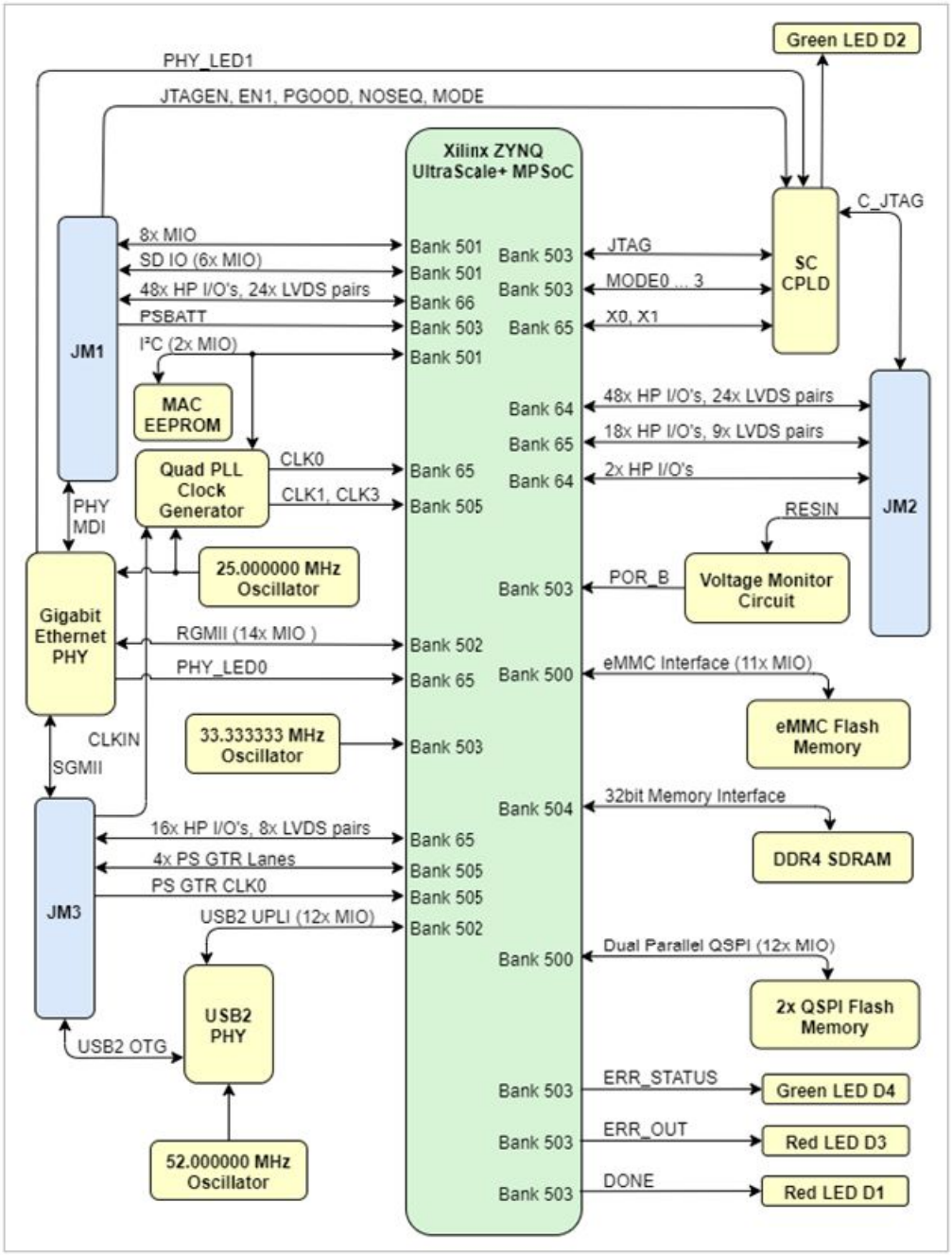


Regarding the usage of our schematics and alike documentation for Trenz module TE0820.

Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified. TE0820is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.

Schematics and other handouts serve for informational purposes only!

trenz electronic	Title: TE0820 - Legal Notices		
A4	Number: TE0820 4BI21KL		Rev. 04
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page 1 of 23
Filename: Legal Notices Modules.SchDoc			



trenz electronic	Title: TE0820 - System Overview		
	A4	Number: TE0820 4BI21KL	Rev. 04
	Date: 2020-10-20	Copyright: Trenz Electronic GmbH	Page 2 of 23
	Filename: Overview.SchDoc		

U_USB-PHY
USB-PHY.SchDoc

U_ETH-PHY
ETH-PHY.SchDoc

U_B_HD
B_HD.SchDoc

U_B64
B64.SchDoc

U_B65
B65.SchDoc

U_B66
B66.SchDoc

U_CONFIG
CONFIG.SchDoc

U_B_MIO
B_MIO.SchDoc

U_B_PS_GT
B_PS_GT.SchDoc

U_CLK
CLK.SchDoc

U_BD
Overview.SchDoc

U_B2B-Connectors
B2B-Connectors.SchDoc

U_eMMC
eMMC.SchDoc

U_PS_DDR
PS_DDR.SchDoc

U_ZU_POWER
ZU_POWER.SchDoc

U_ZU_PS_POWER
ZU_PS_POWER.SchDoc

U_DDR4-RAM_2
DDR4-RAM_2.SchDoc

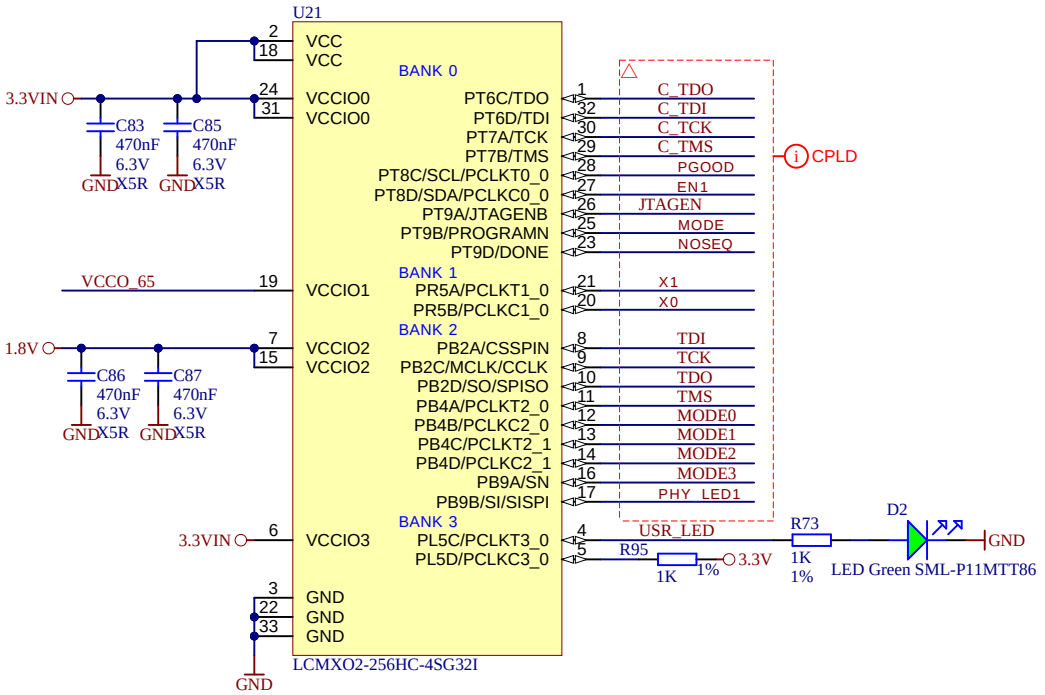
U_DDR4-RAM
DDR4-RAM.SchDoc

U_POWER
POWER.SchDoc

U_POWER_1
POWER_1.SchDoc

U_REV_CH
Revision Changes.SchDoc

U_LN
Legal Notices Modules.SchDoc

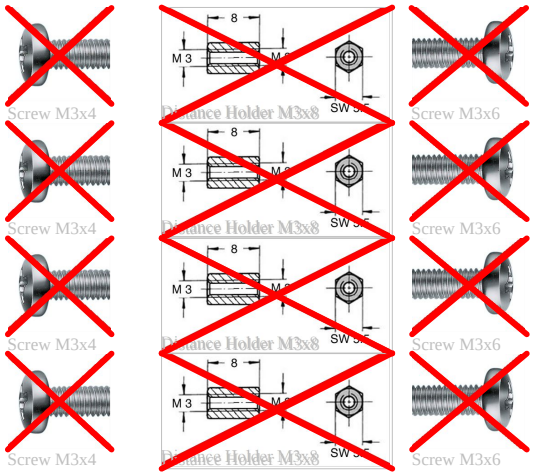


Special notes:

Serial
Serial
Serialnumber 6,3 x 6.3mm

PM1
FIDU-DOT - mini
PM2
FIDU-DOT - mini
PM3
FIDU-DOT - mini
PM4
FIDU-DOT - mini
PM5
FIDU-DOT - mini
PM6
FIDU-DOT - mini

Mount.Hole 3.2mm
GND
Mount.Hole 3.2mm
GND
Mount.Hole 3.2mm
GND
Mount.Hole 3.2mm
GND



Assembly variant	4BI21KL
Created by	VY
Modified by	VY
Modified at	2020-10-27
SVN Revision	12571

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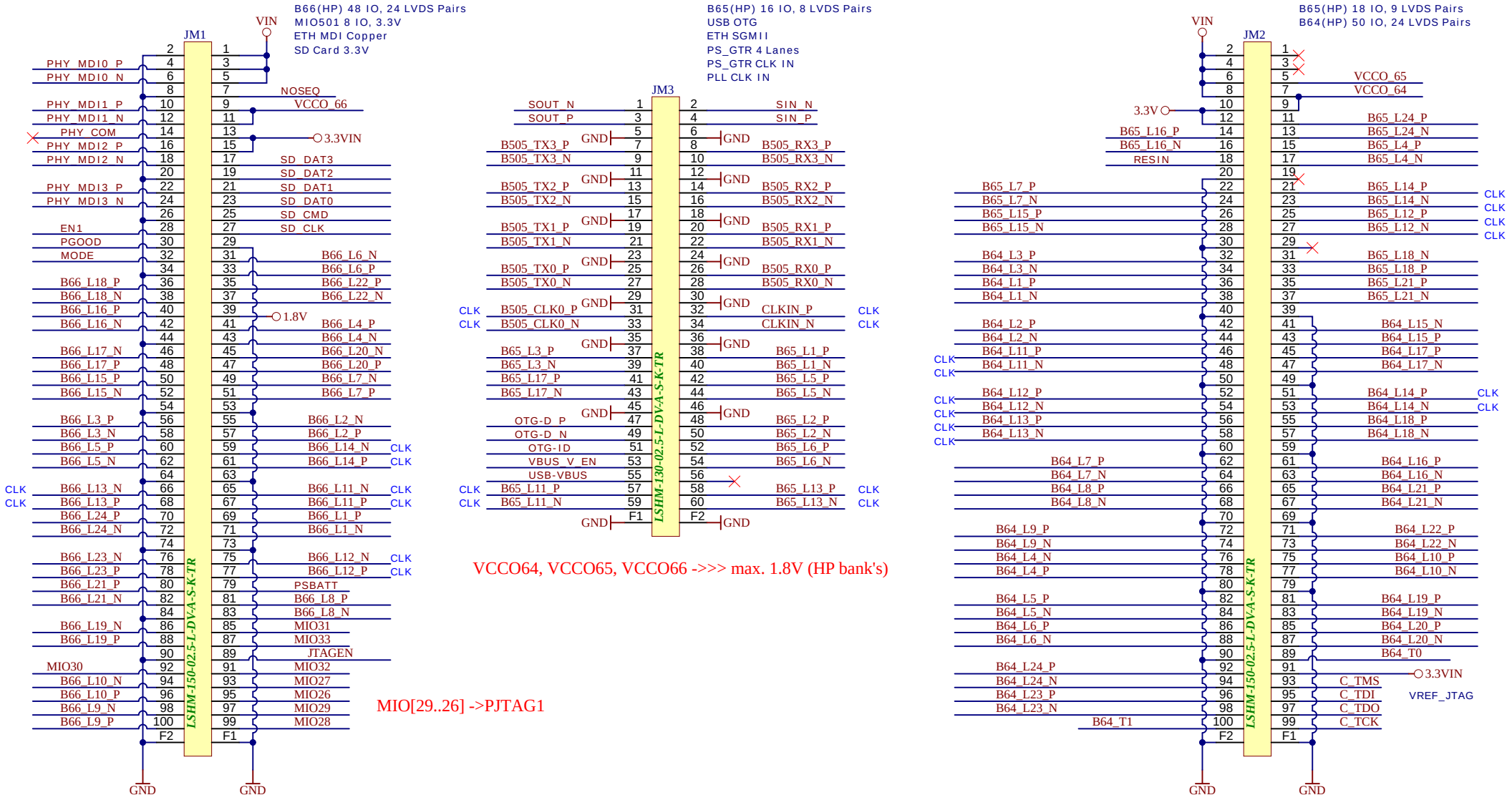
Title: TE0820		
A4	Number: TE0820 4BI21KL	Rev. 04
Date: 2020-07-16	Copyright: 2015 Trenz Electronic GmbH	Page3 of 23
Filename: TE0820.SchDoc		



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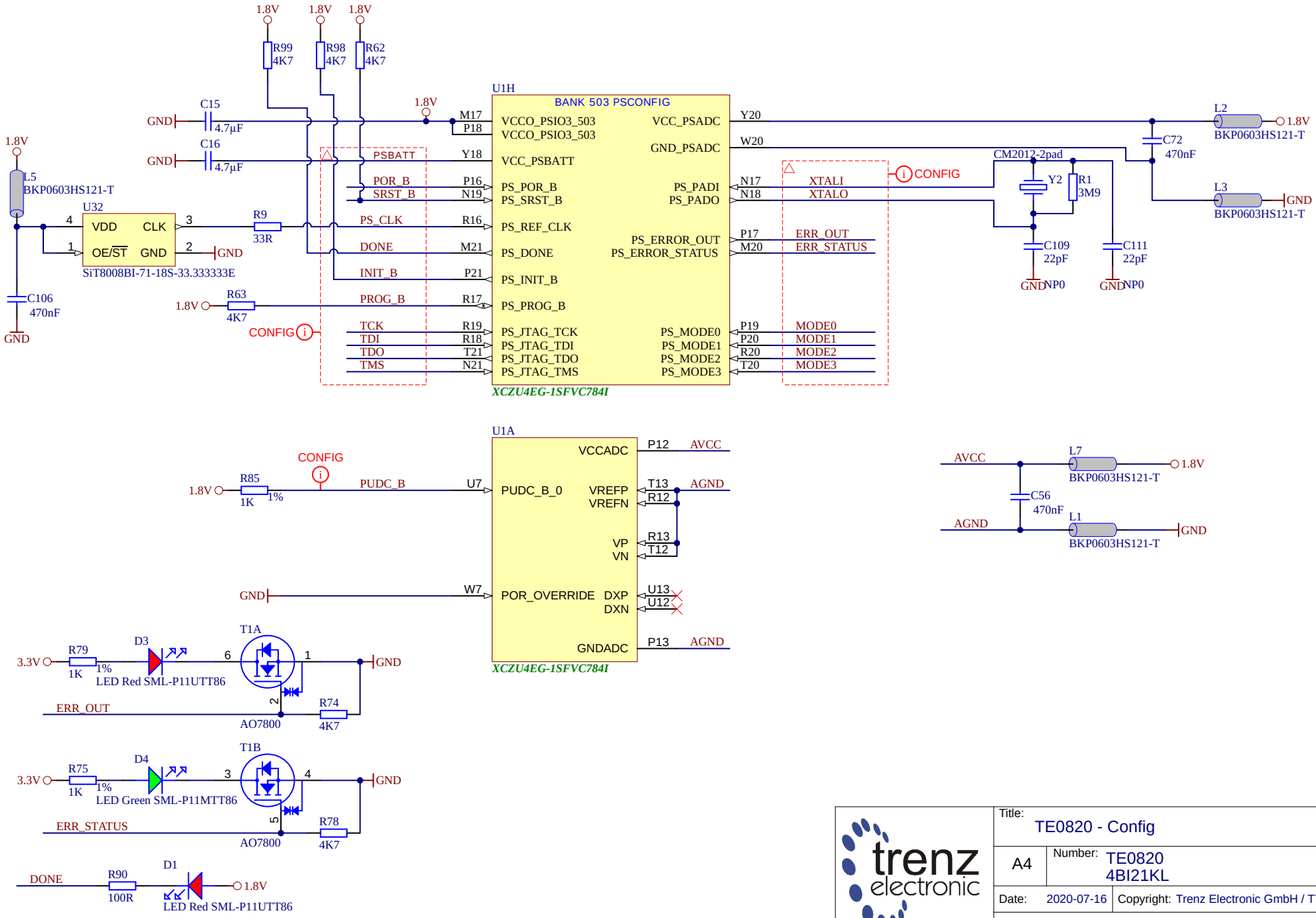
Date: 2020-07-16	Copyright: 2015 Trenz Electronic GmbH		Page4 of 23
Filename: B2B-Connectors.SchDoc			



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Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT	Page5 of 23
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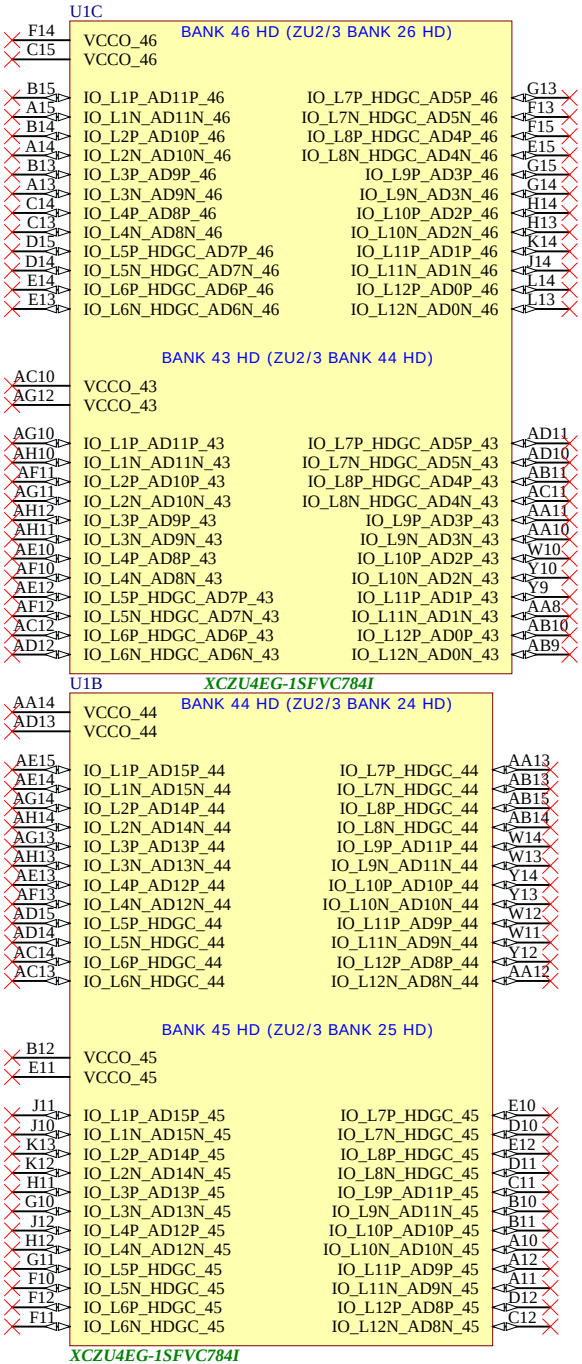

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Title: TE0820 - HD Banks			
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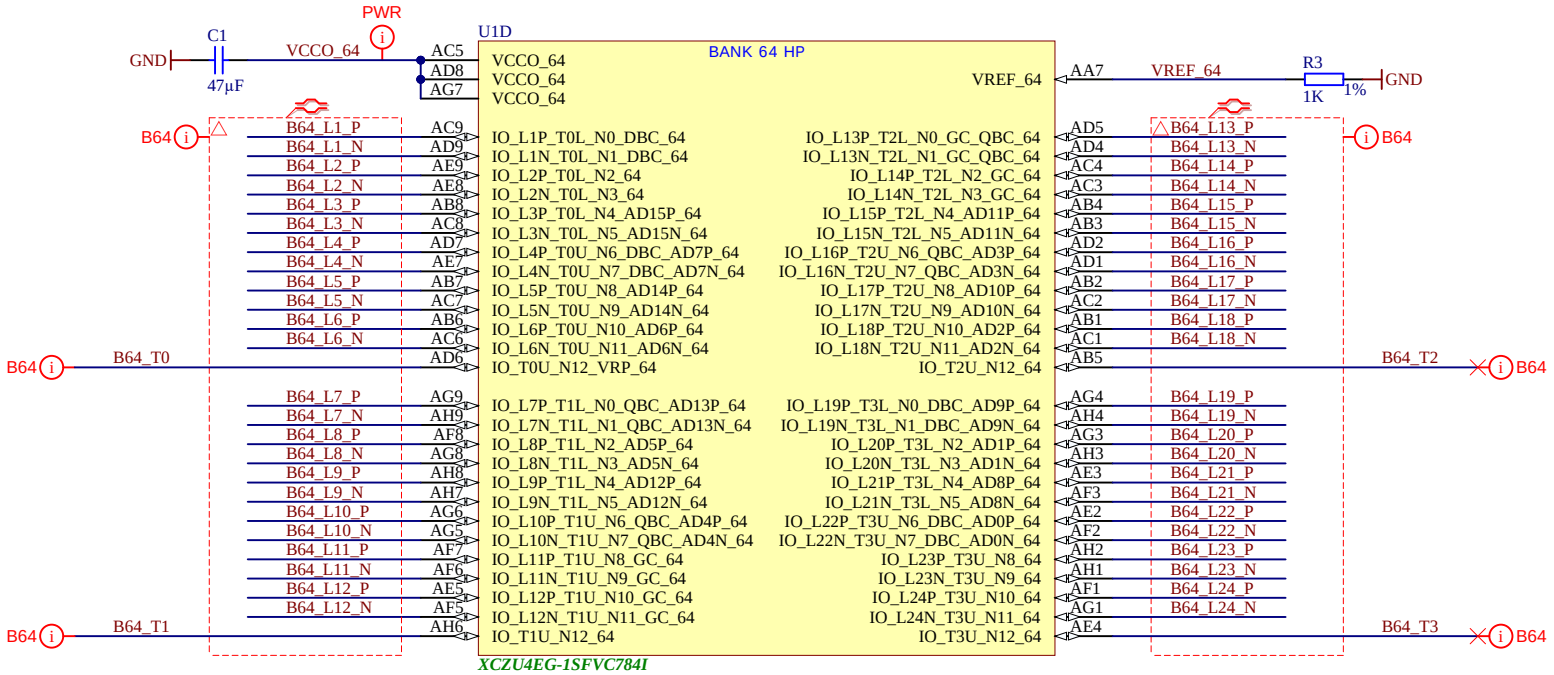
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page6 of 23
Filename: B_HD.SchDoc			



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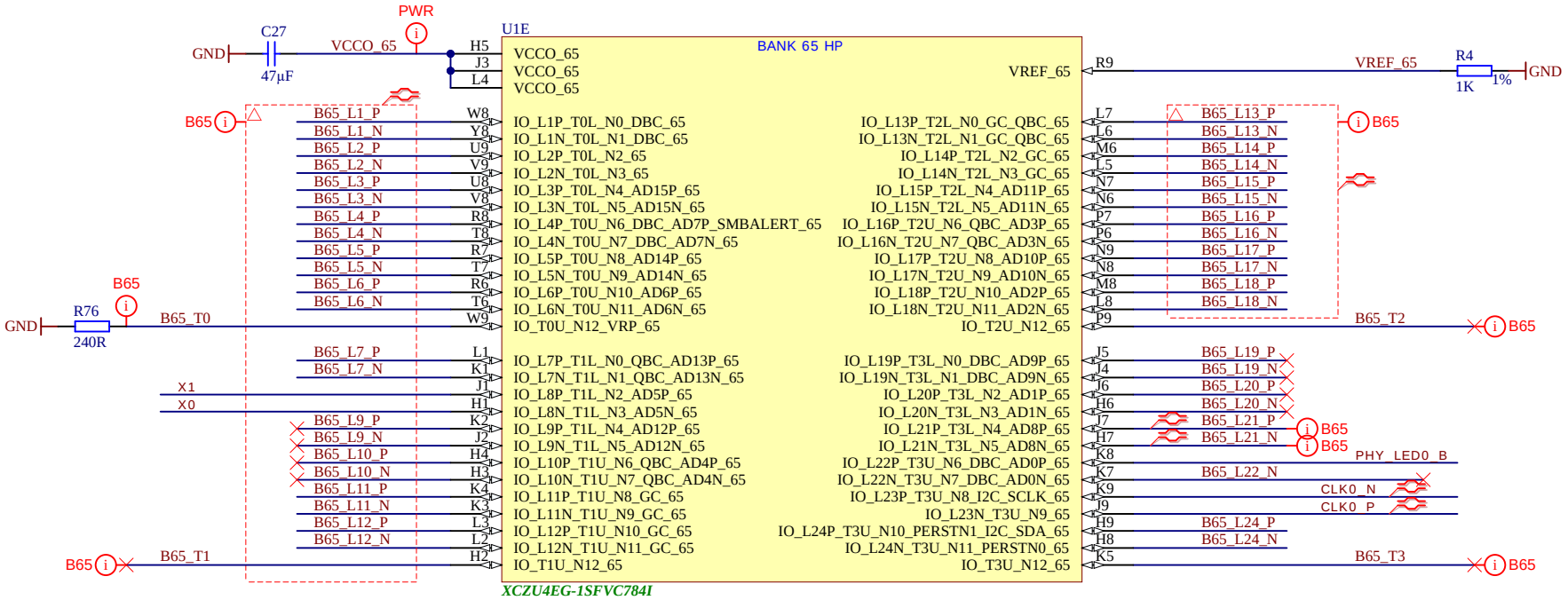
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page7 of 23
Filename: B64.SchDoc			



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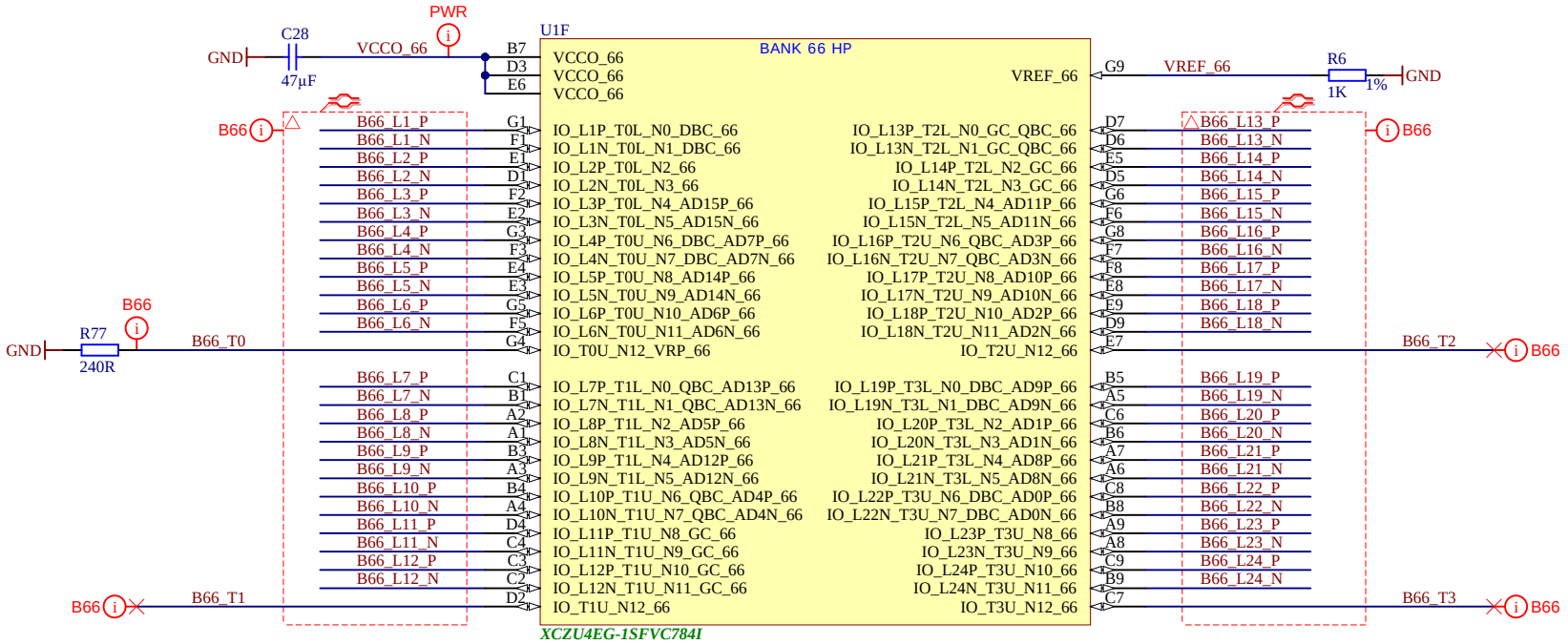

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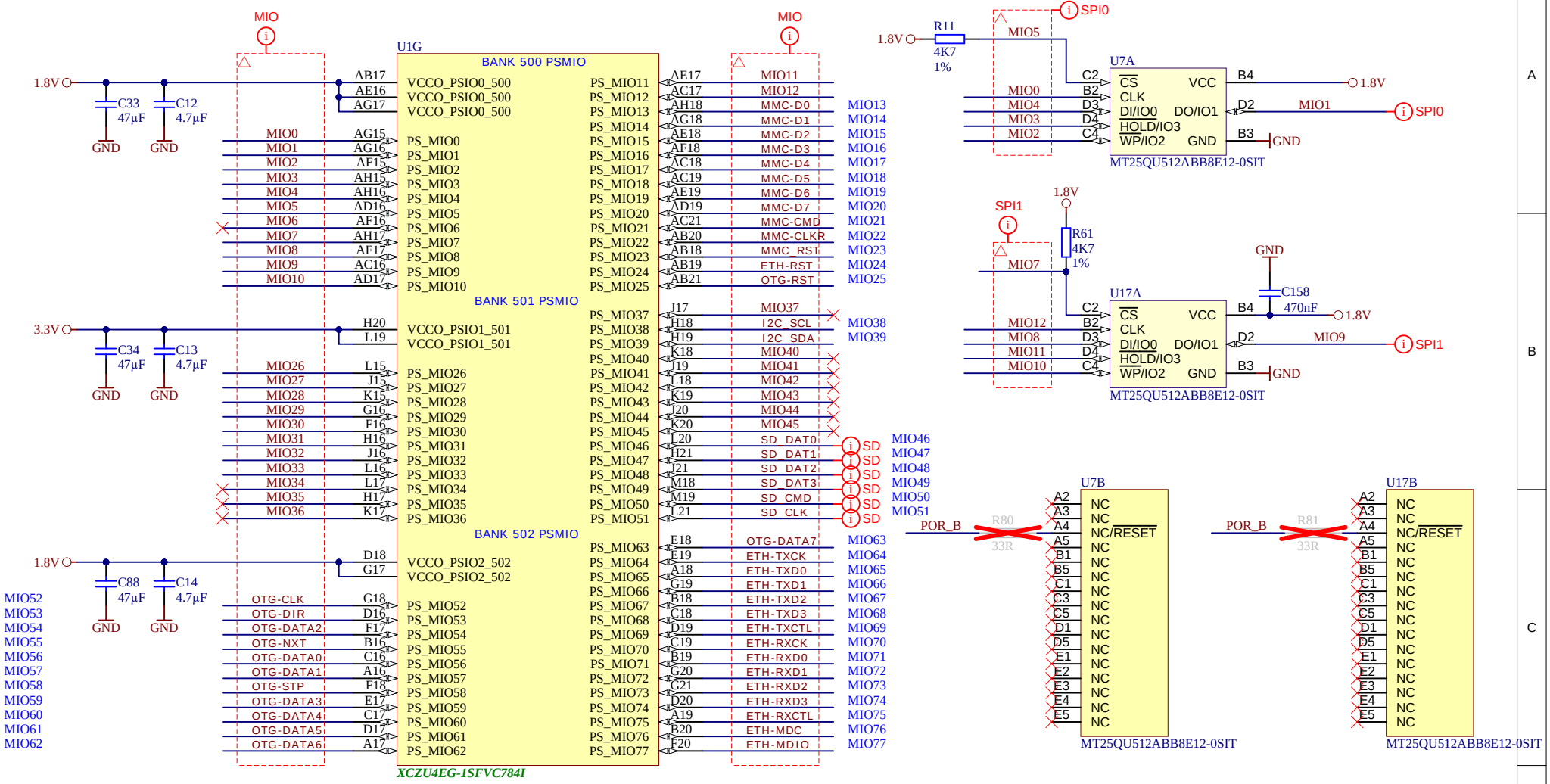
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page9 of 23
Filename: B66.SchDoc			



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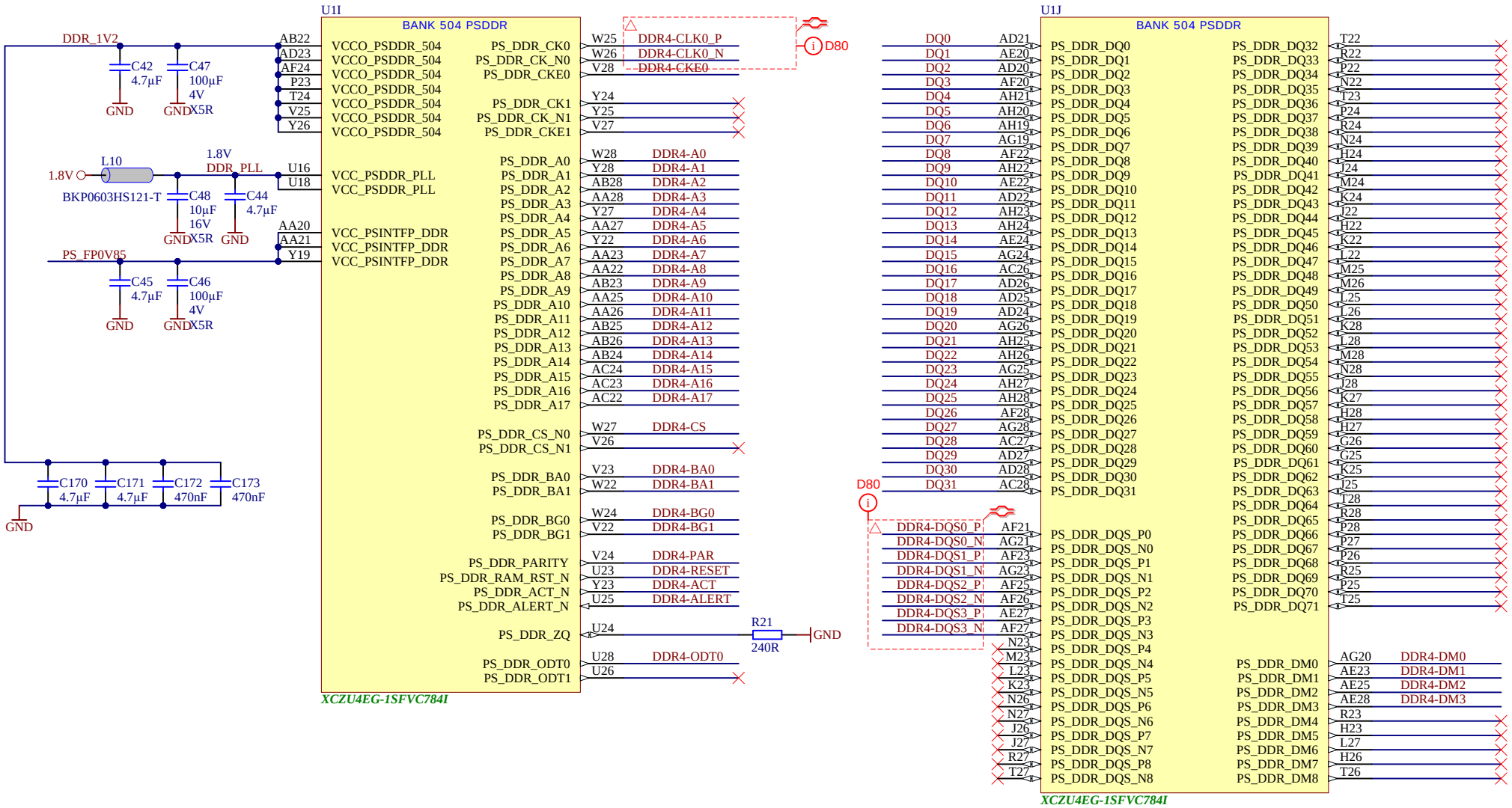
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page10 of 23
Filename: B_MIO.SchDoc			



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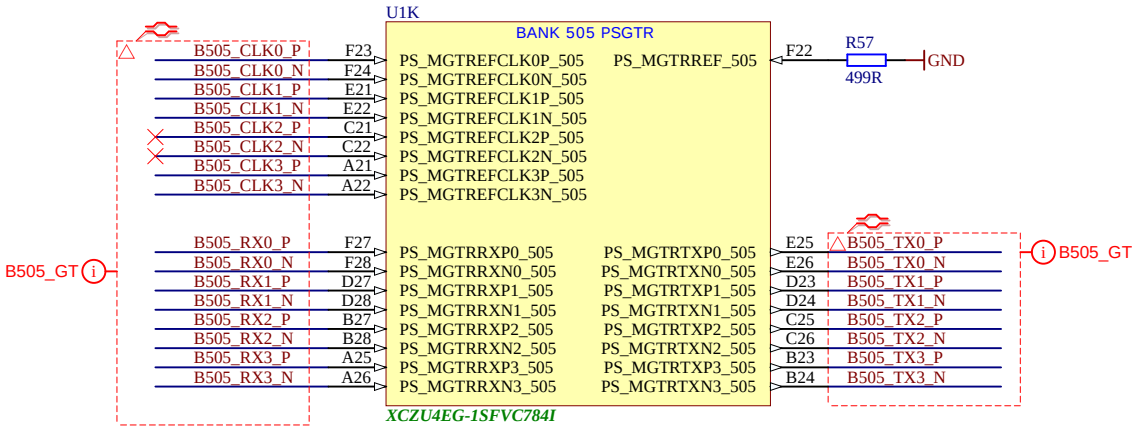
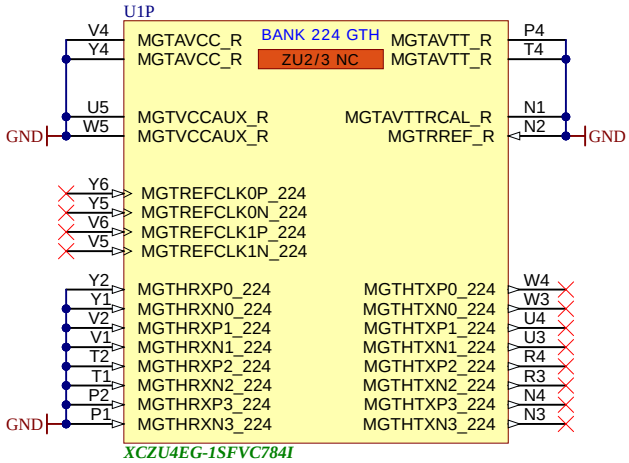
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page11 of 23
Filename:	PS_DDR.SchDoc		





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Title: TE0820 - PS_GT		
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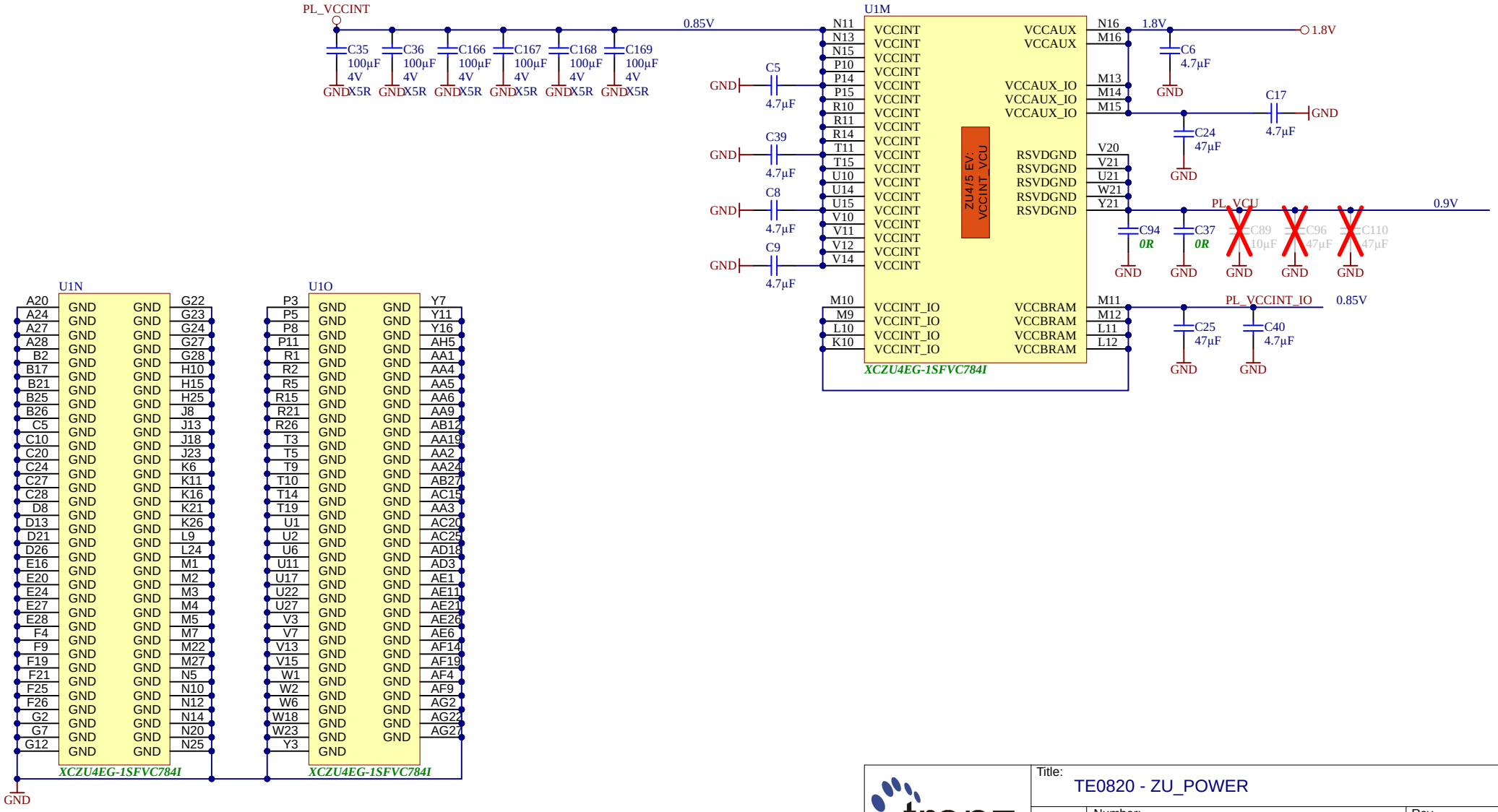
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT	Page12 of 23
Filename: B_PS_GT.SchDoc		



Title: TE0820 - ZU_POWER		
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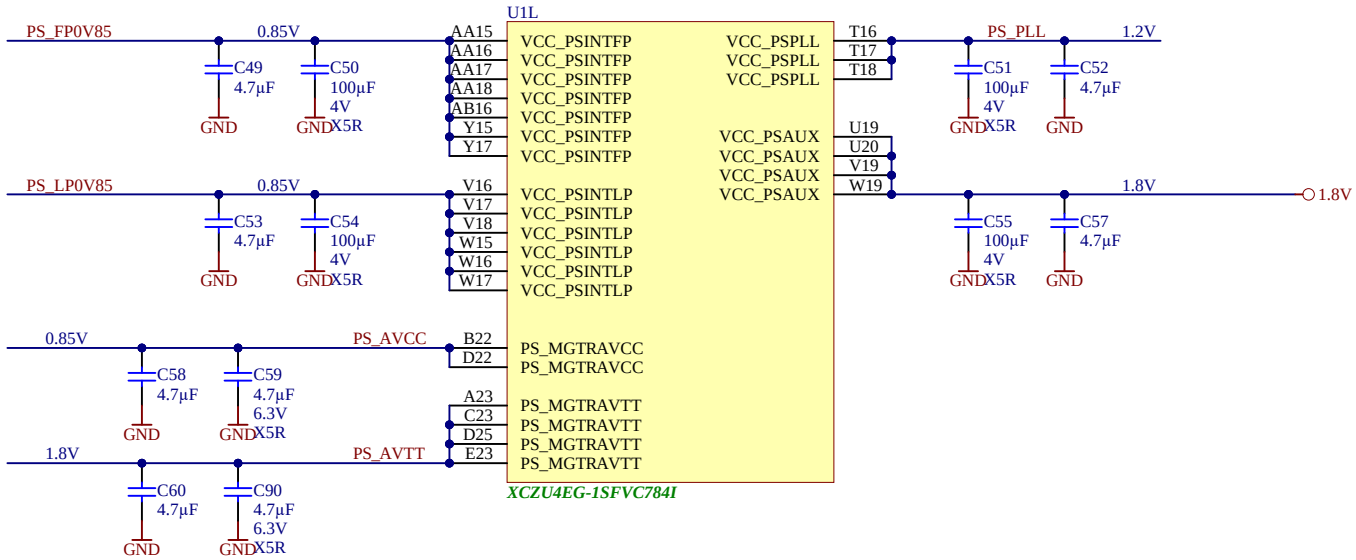
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT	Page13 of 23
Filename: ZU_POWER.SchDoc		



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Title: TE0820 - ZU_PS_POWER		
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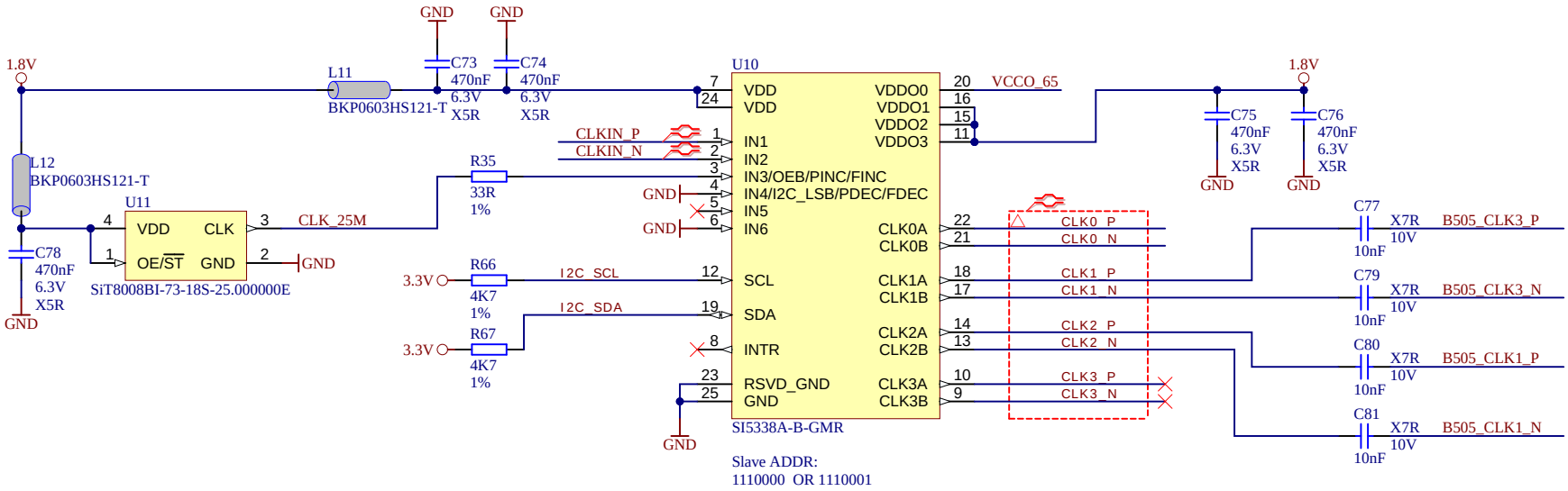
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT	Page14 of 23
Filename: ZU_PS_POWER.SchDoc		



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Date: 2020-07-16	Copyright: 2015 Trenz Electronic GmbH		Page15 of 23
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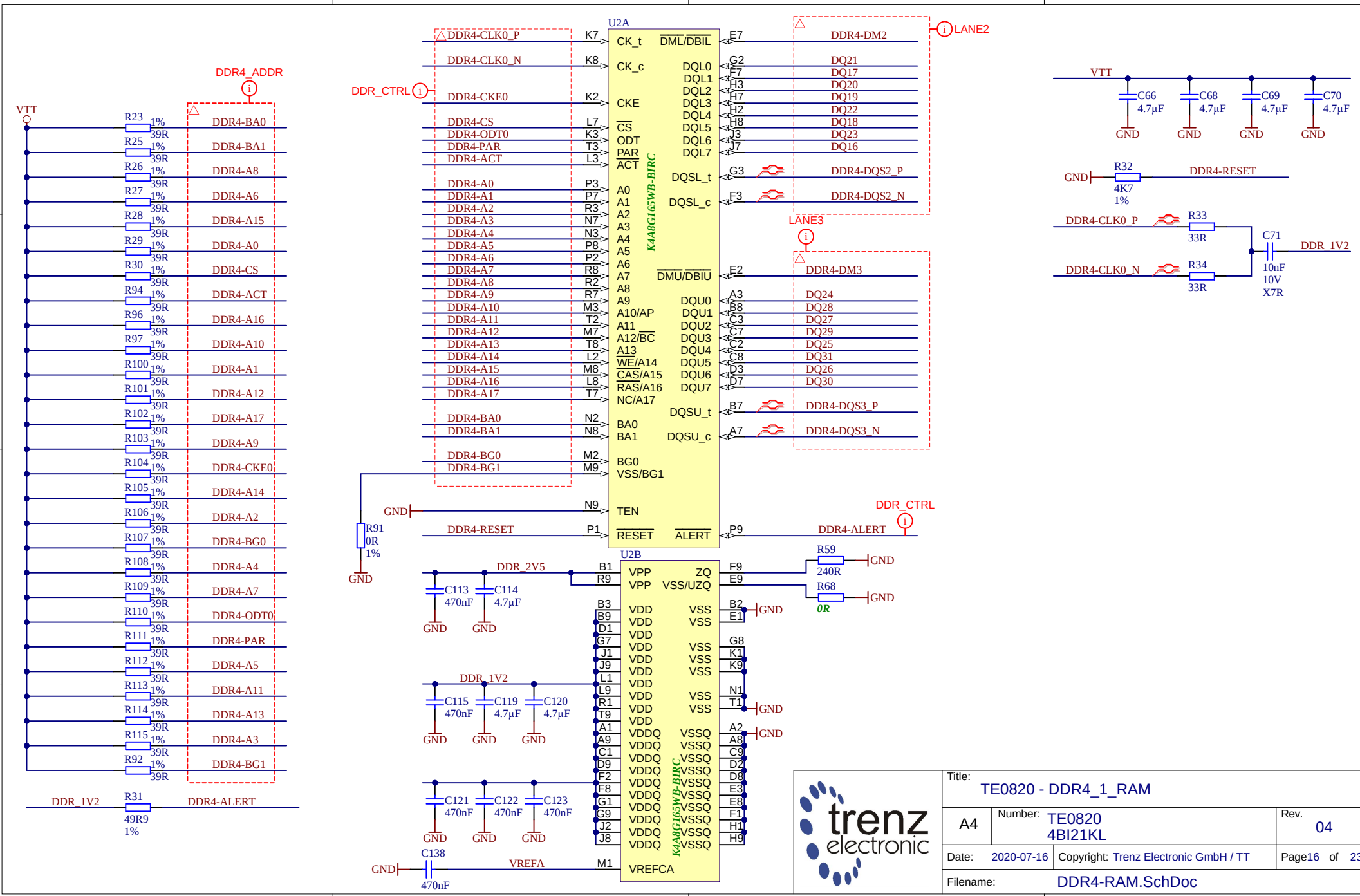

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Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page16 of 23
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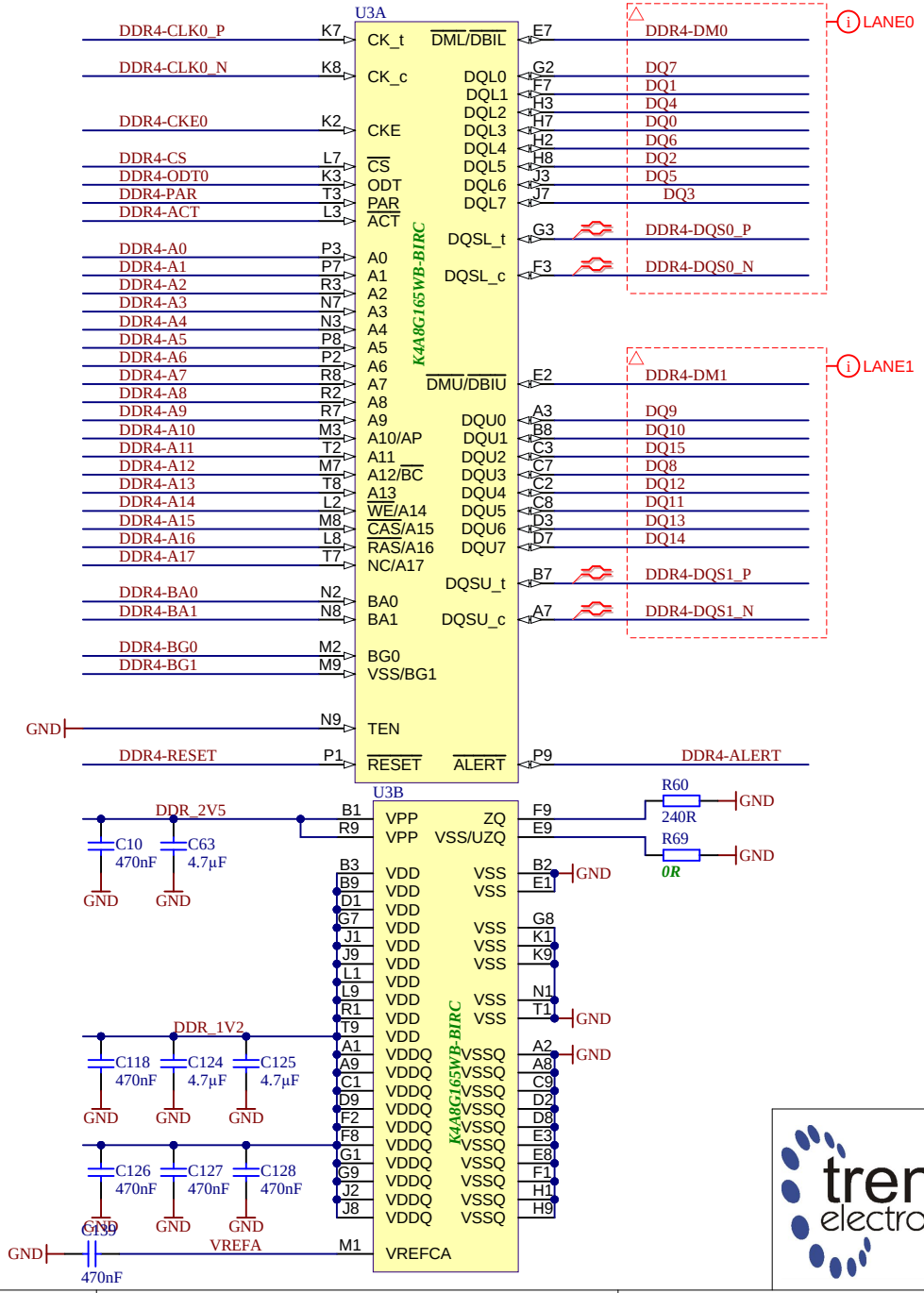

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Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page17 of 23
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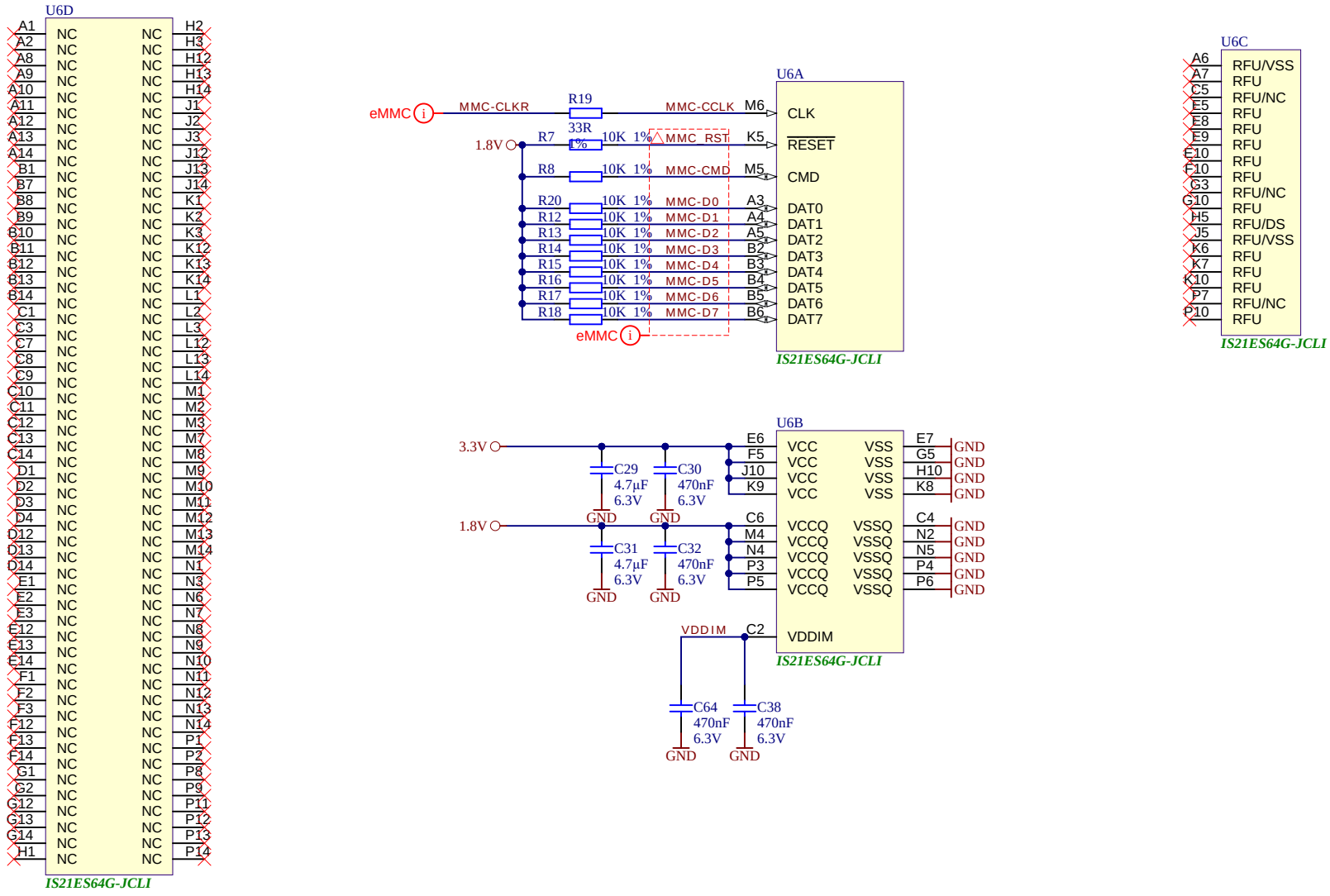

trenz electronic	Title: TE0820 - eMMC		
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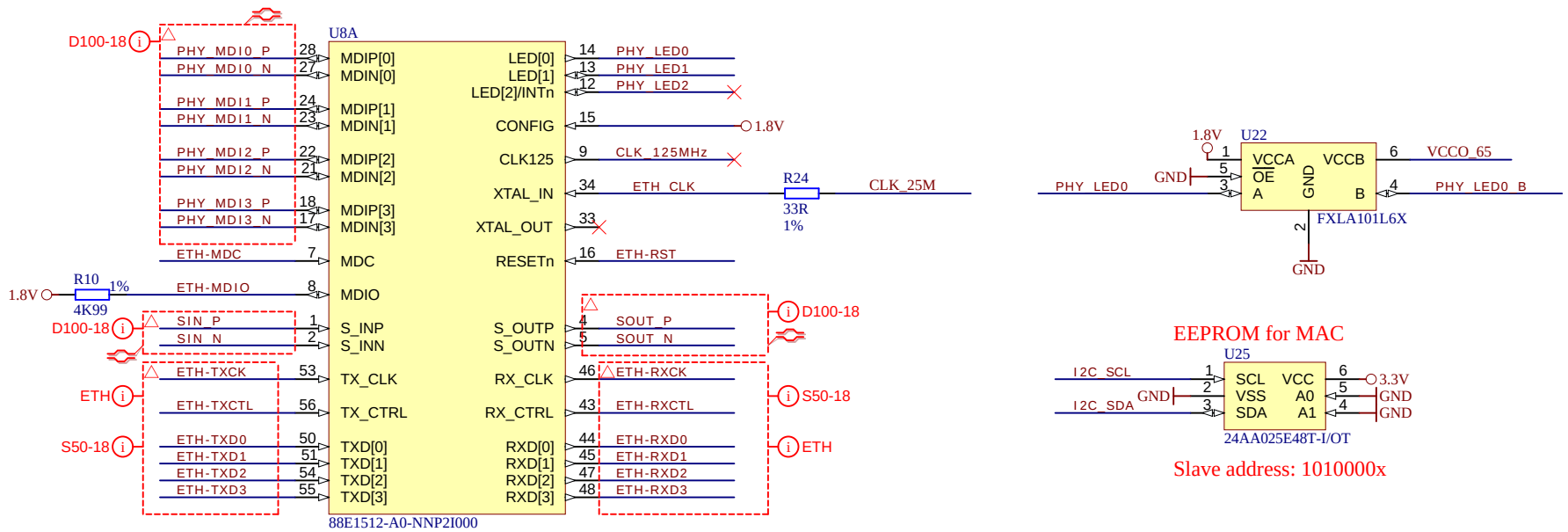
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page18 of 23
Filename: eMMC.SchDoc			



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Date: 2020-07-16	Copyright: 2015 Trenz Electronic GmbH		Page19 of 23
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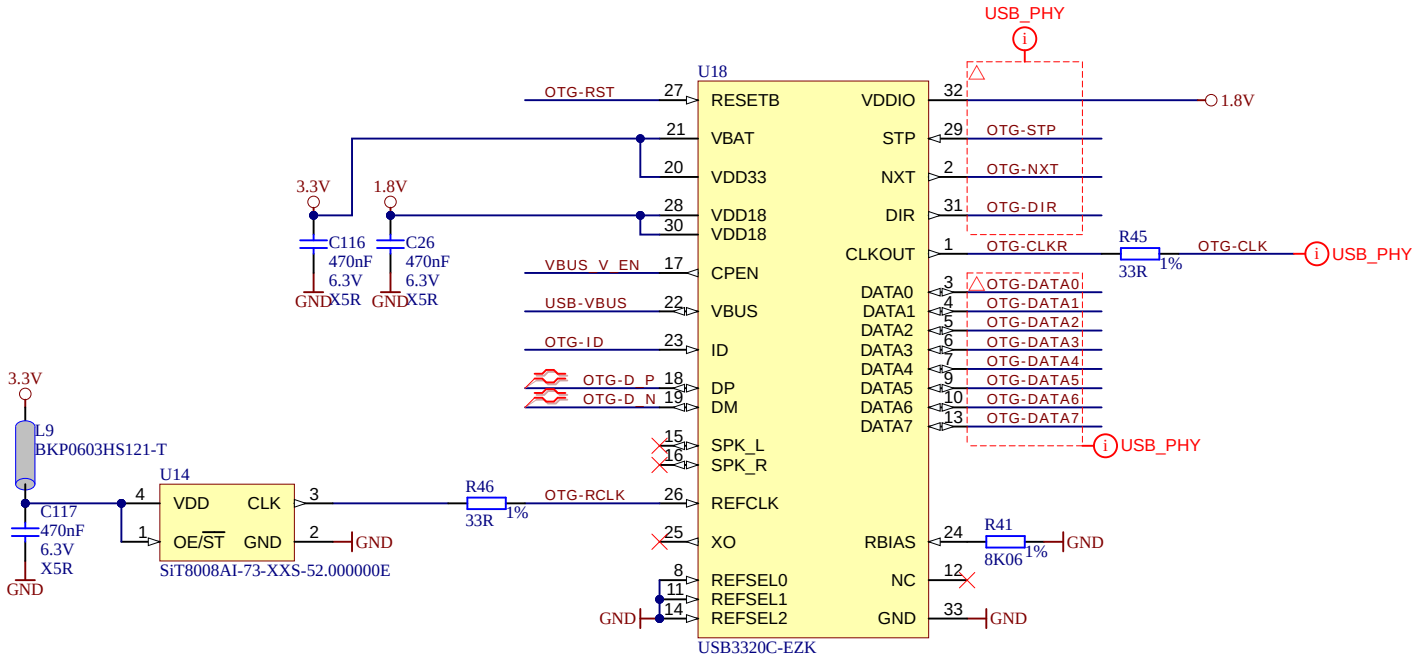

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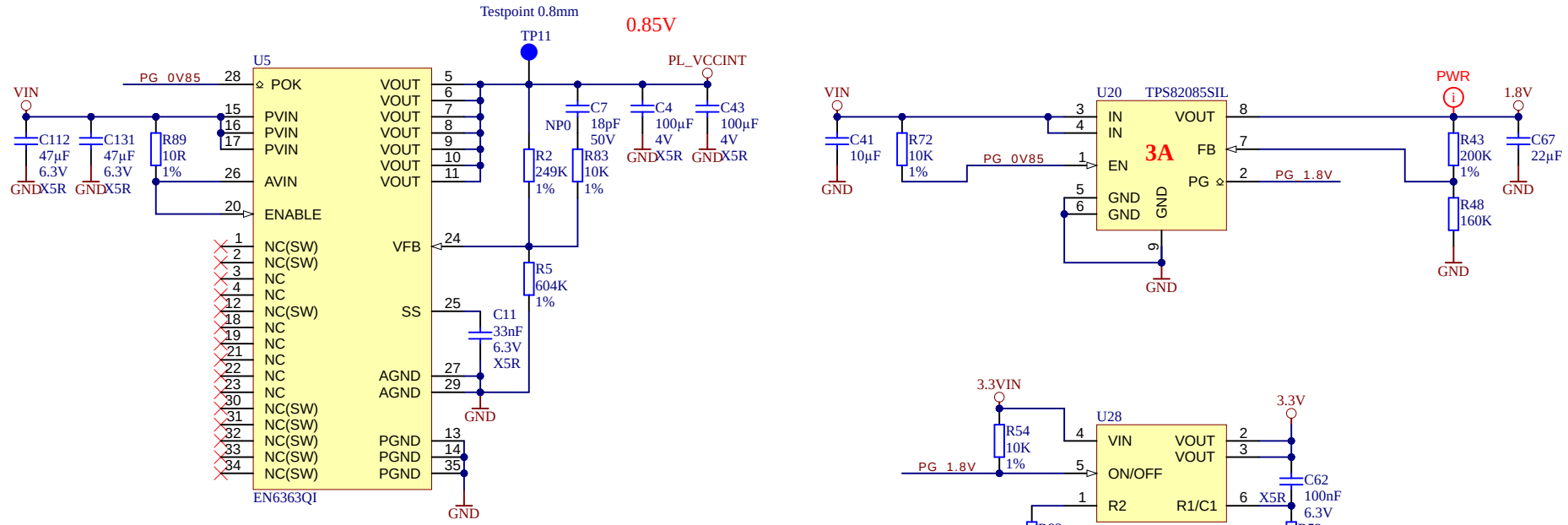

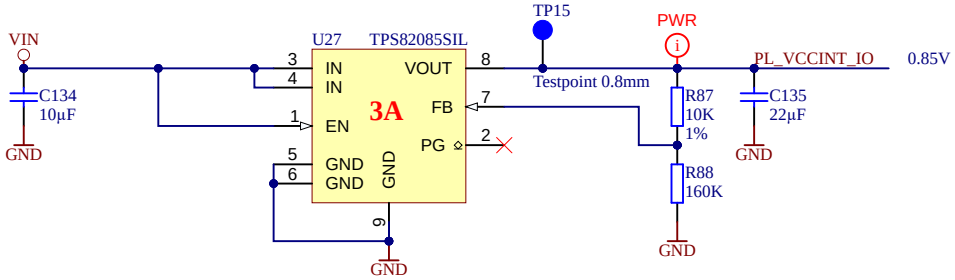

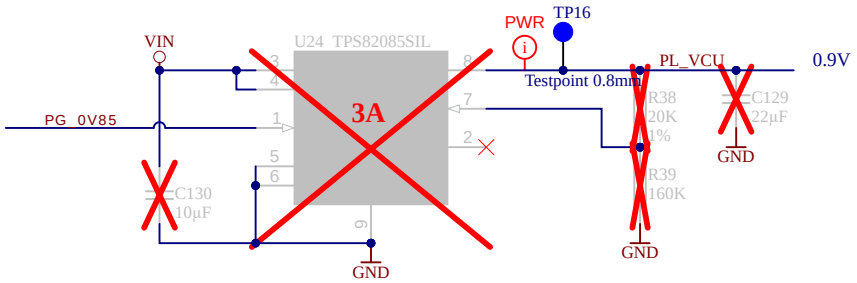

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Title: TE0820 - POWER			
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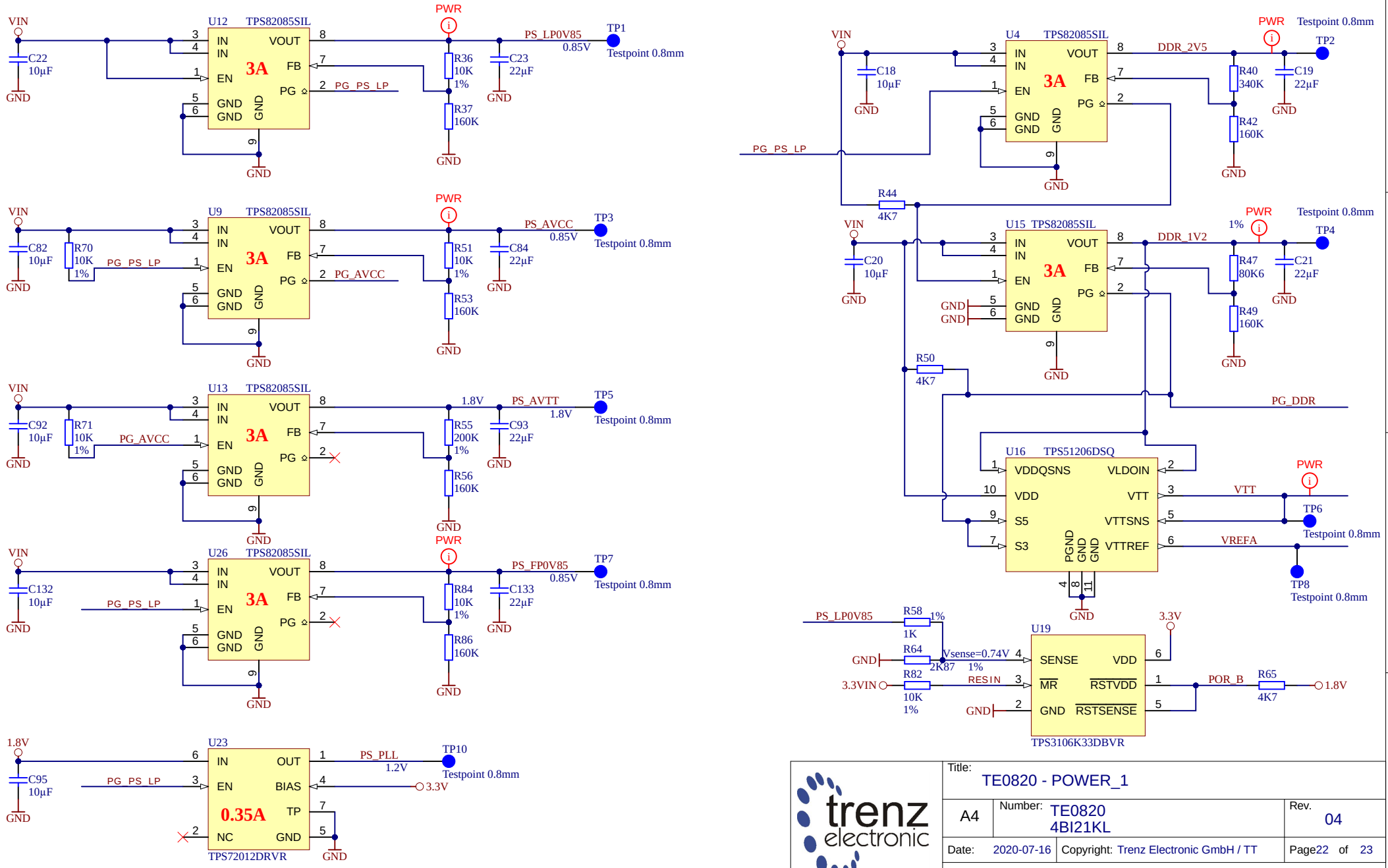
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page21 of 23
Filename: POWER.SchDoc			



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Title:	TE0820 - POWER_1		
A4	Number: TE0820 4BI21KL		Rev. 04
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT		Page22 of 23
Filename:	POWER_1.SchDoc		

CHANGES REV01 to REV02
---
1) Added MAC EEPROM (slave address:)
2) LIB components update
3) Fixed SD Card connection
4) Fixed sense connection from DCDC
5) Made correct power connection for VCU (removed DCDC, added resistors and caps like as Xilinx recommended)
6) Added resistors for variants (ZU+ with/without VCU)
7) Added termination resistors (240R) to VRP pins fro all HP-banks

CHANGES REV02 to REV03
---
1) Fixed VCU connection: add additional DCDC (0.9V)
2) LIB components update
3) Change package 1K resistors (0402 -> 0201)
4) Added LEDs (1x user LED, 1x LED for ERR_STATUS, 1xLED for ERR_OUT)
5) Change obsolete 2xSPI Flash (256MBit) -> 2xSPI Flash (512MBit)
6) Added additional DCDCs (PL_VCCINT_IO, PS_FP0V85)
7) Changed DCDC (U5) 6A (optional 4A)

CHANGES REV03 to REV04

VT: 1) Fixed DDR4 connection (BG1), support B-die DDR4 Industrial grade chips
VT: 2) Added R93, changed value C62, change obsolete U28
VT: 3) Added R89 (10R)
VT: 4) Added additional caps 4.7uF to PS_AVTT/PS_AVCC (Xilinx doc UG583)
VT: 5) Changed R51 20k ->10K (PS_AVCC = 0.85V, Xilinx doc DS925 v1.17)
VT: 6) Fixed DDR4 connection (Alert)
VT: 7) Added 3.3V signal to CPLD
VT: 8) Added testpoints
VT: 9) LIB components update

Revision 04a (29.10.2020):
---
1. VY: added block diagram, updated module pictures

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Title: TE0820 - Revision Changes		
A4	Number: TE0820 4BI21KL	Rev. 04
Date: 2020-07-16	Copyright: Trenz Electronic GmbH / TT	Page23 of 23
Filename: Revision Changes.SchDoc		