Special notes:

U_Power
Power.SchDoc

U_FPGA_PWR
FPGA_PWR.SchDoc

U_DDR3_RAM
DDR3_RAM.SchDoc

U_FPGA_CFG_CLK
FPGA_CFG_CLK.SchDoc

U_FPGA_B3
FPGA_B3.SchDoc

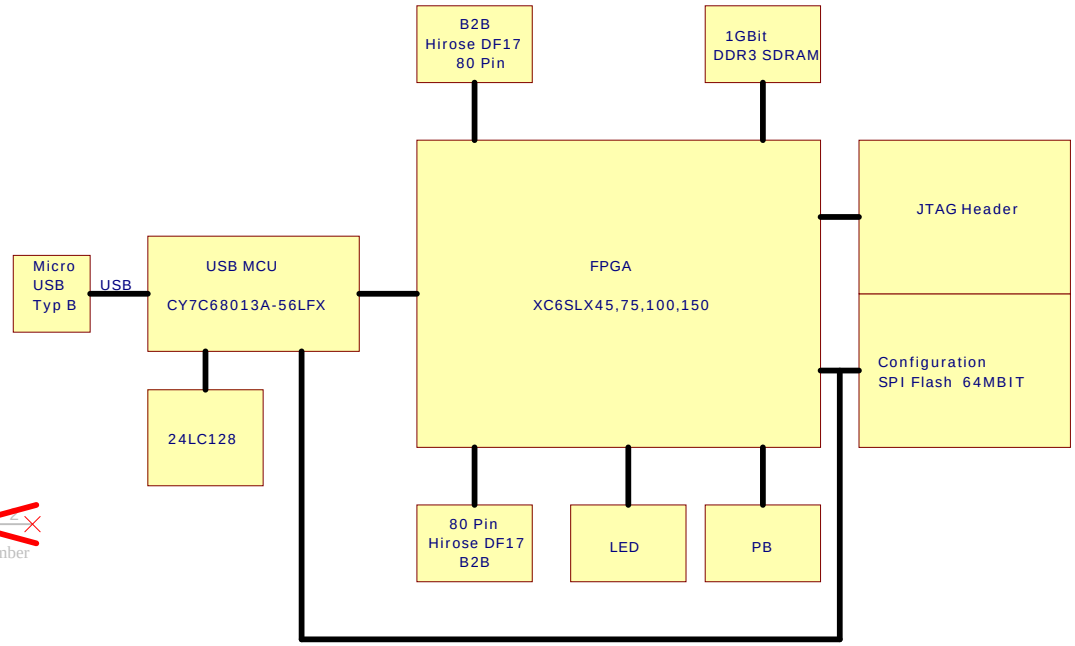
U_FPGA_DDR3
FPGA_DDR3.SchDoc

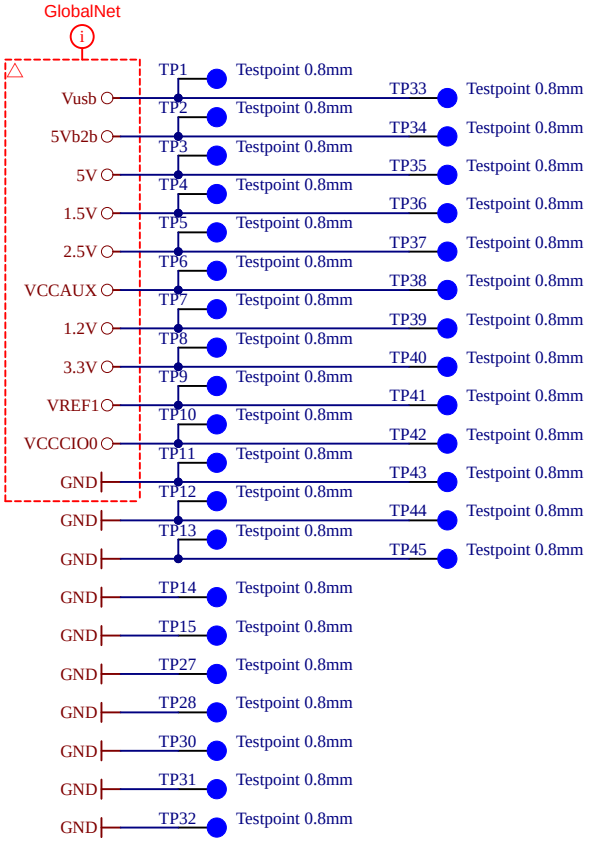
U_FPGA_B2
FPGA_B2.SchDoc

U_FPGA_B0
FPGA_B0.SchDoc

U_USB
USB.SchDoc

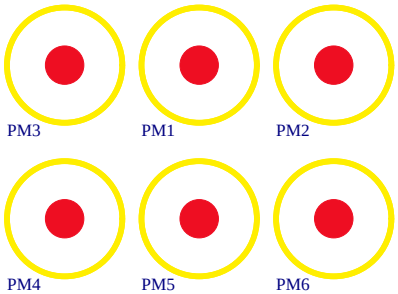
U_B2B_Connectors
B2B_Connectors.SchDoc

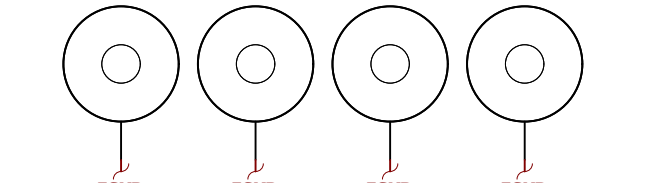




Serial
Serial
Serialnumber 6,3 x 6.3mm

S/N1
Serialnumber





LOGO1
TE Logo PRINT Layer
LOGO PRINT

Assembly variant	IV-DWS
Created by	VY
Modified by	VY
Modified at	2020-10-19
SVN Revision	

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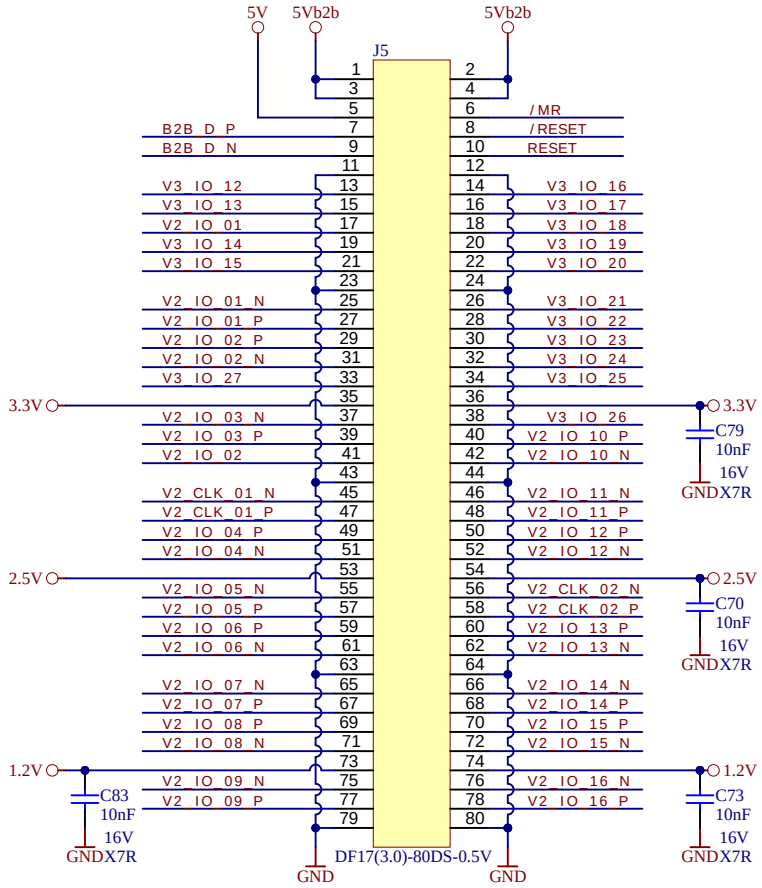

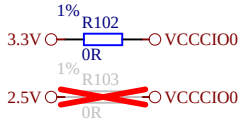

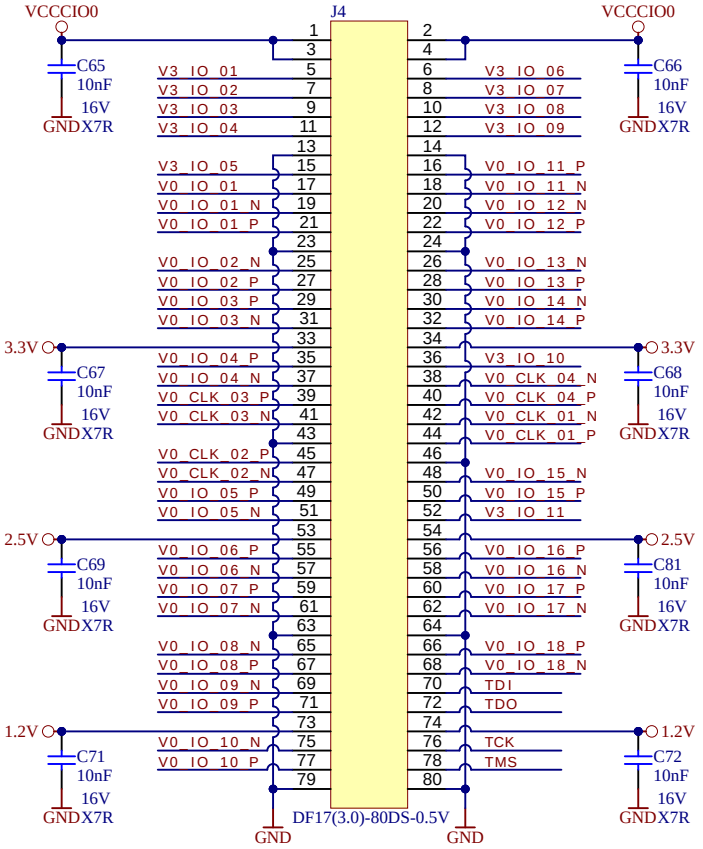

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Title: TE0630 - B2B_Connectors		
A4	Number: TE0630 IV-DWS	Rev. 02
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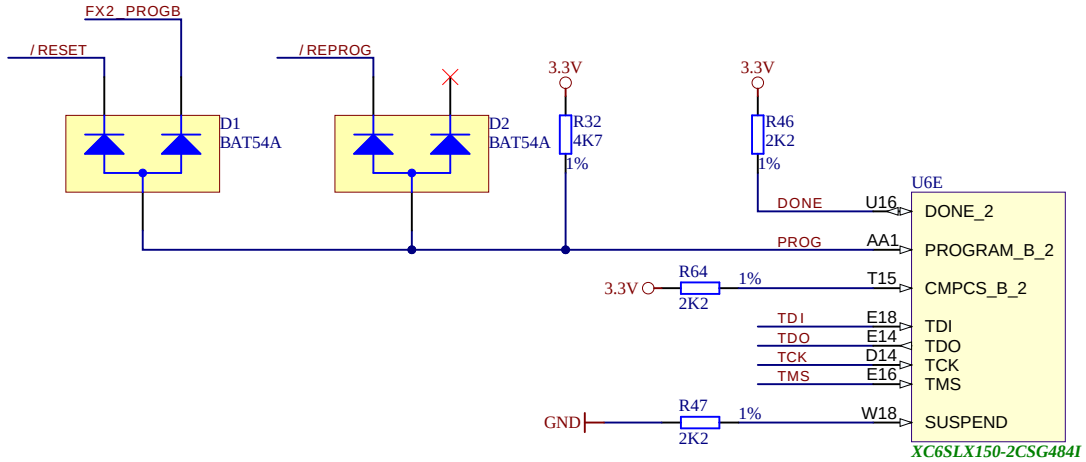
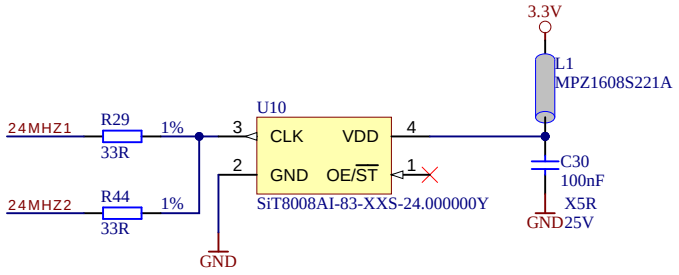
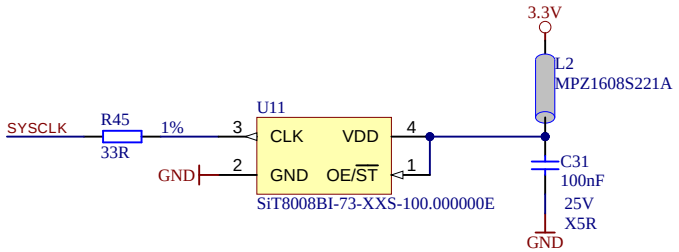
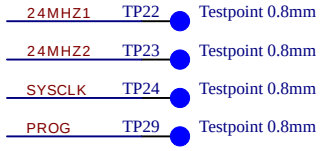
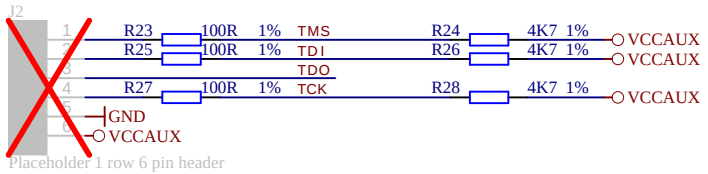
Filename: B2B_Connectors.SchDoc		











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Title: TE0630 - FPGA_CFG_CLK		
A4	Number: TE0630 IV-DWS	Rev. 02
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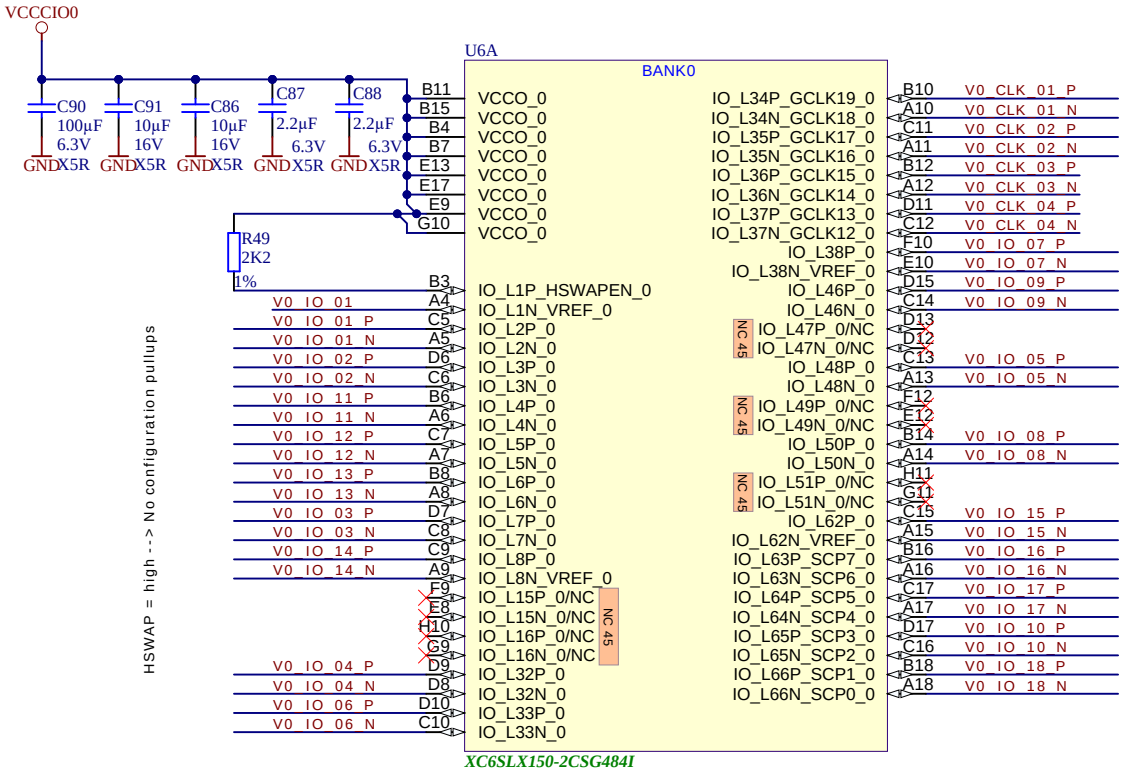
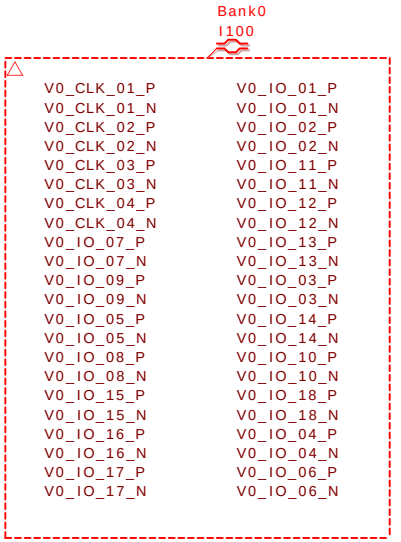
Filename: FPGA_CFG_CLK.SchDoc		





trenz electronic	Title: TE0630 - FPGA_B0		
A4	Number: TE0630 IV-DWS		Rev. 02
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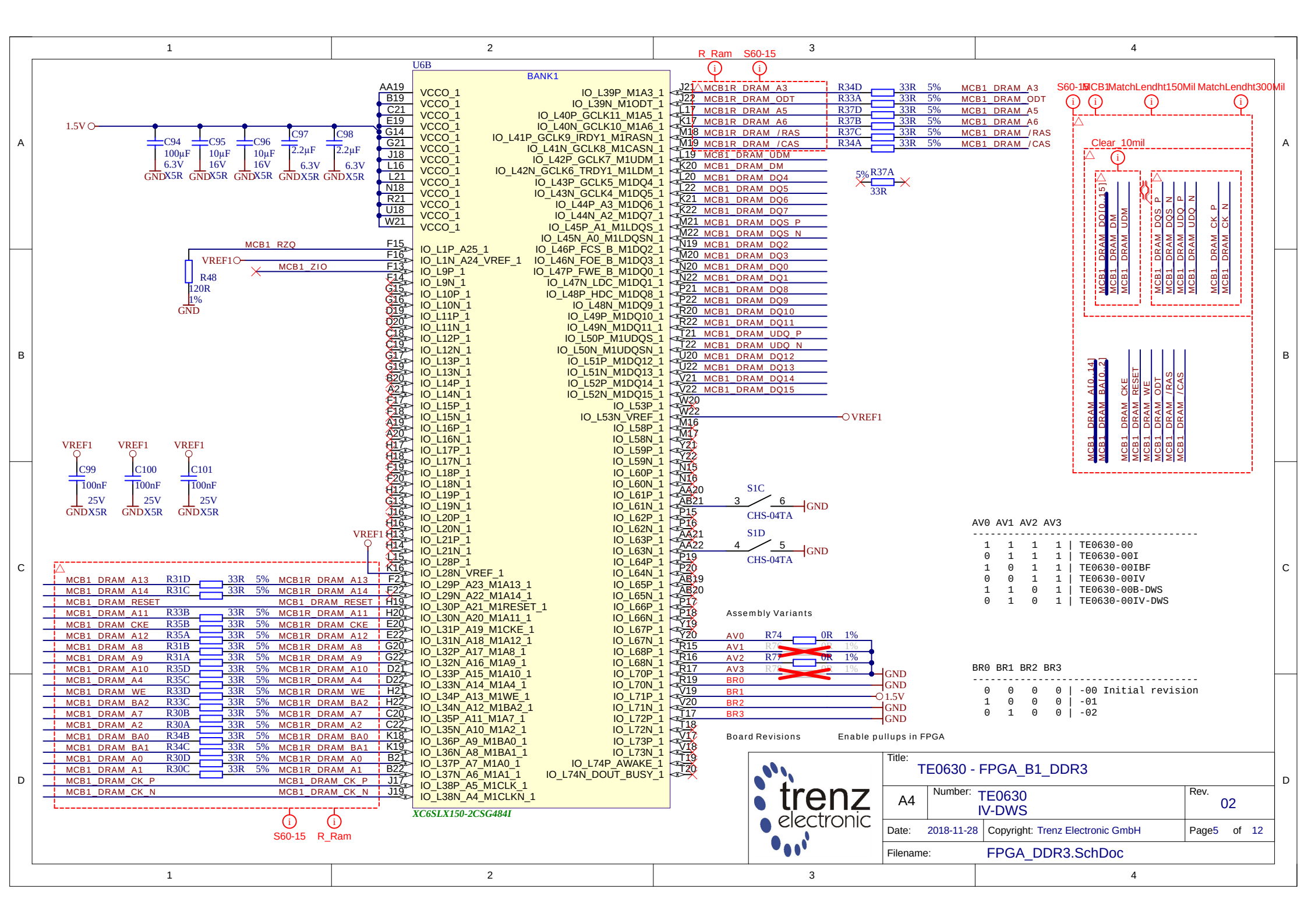
Filename: FPGA_B0.SchDoc			



Title: TE0630 - FPGA_B1_DDR3			
A4	Number: TE0630 IV-DWS		Rev. 02
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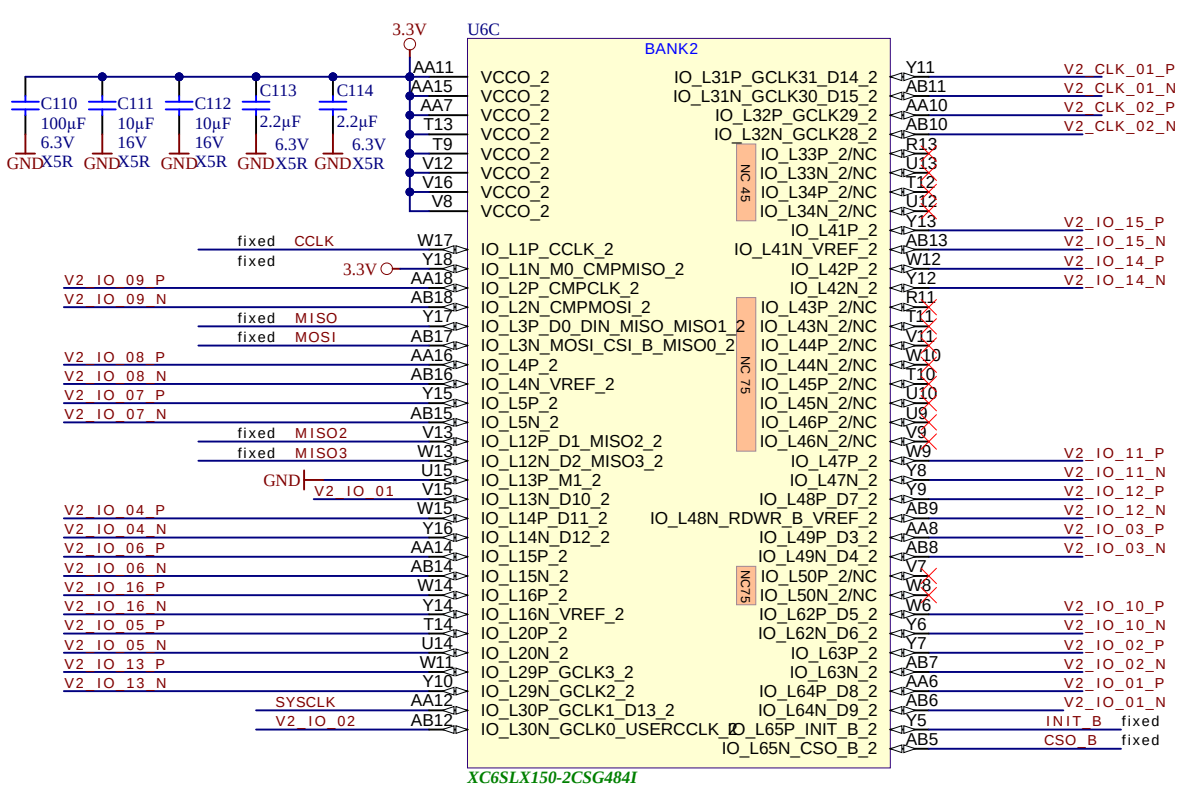
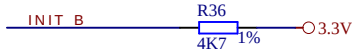
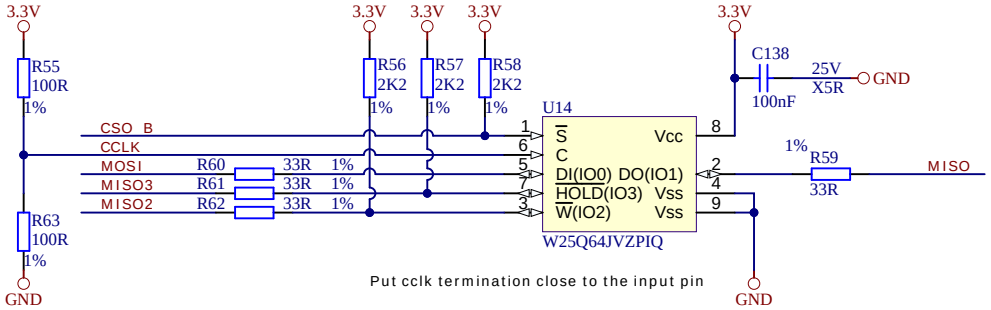
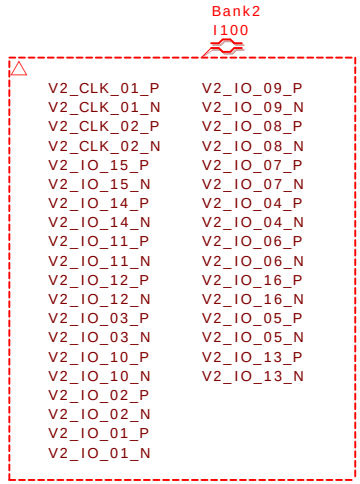
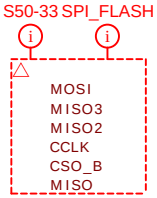
Filename: FPGA_DDR3.SchDoc			

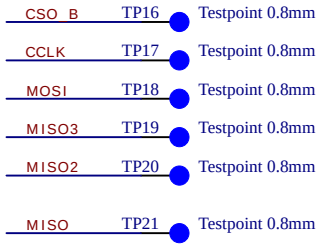












Title: TE0630 - FPGA_B2			
A4	Number: TE0630 IV-DWS		Rev. 02
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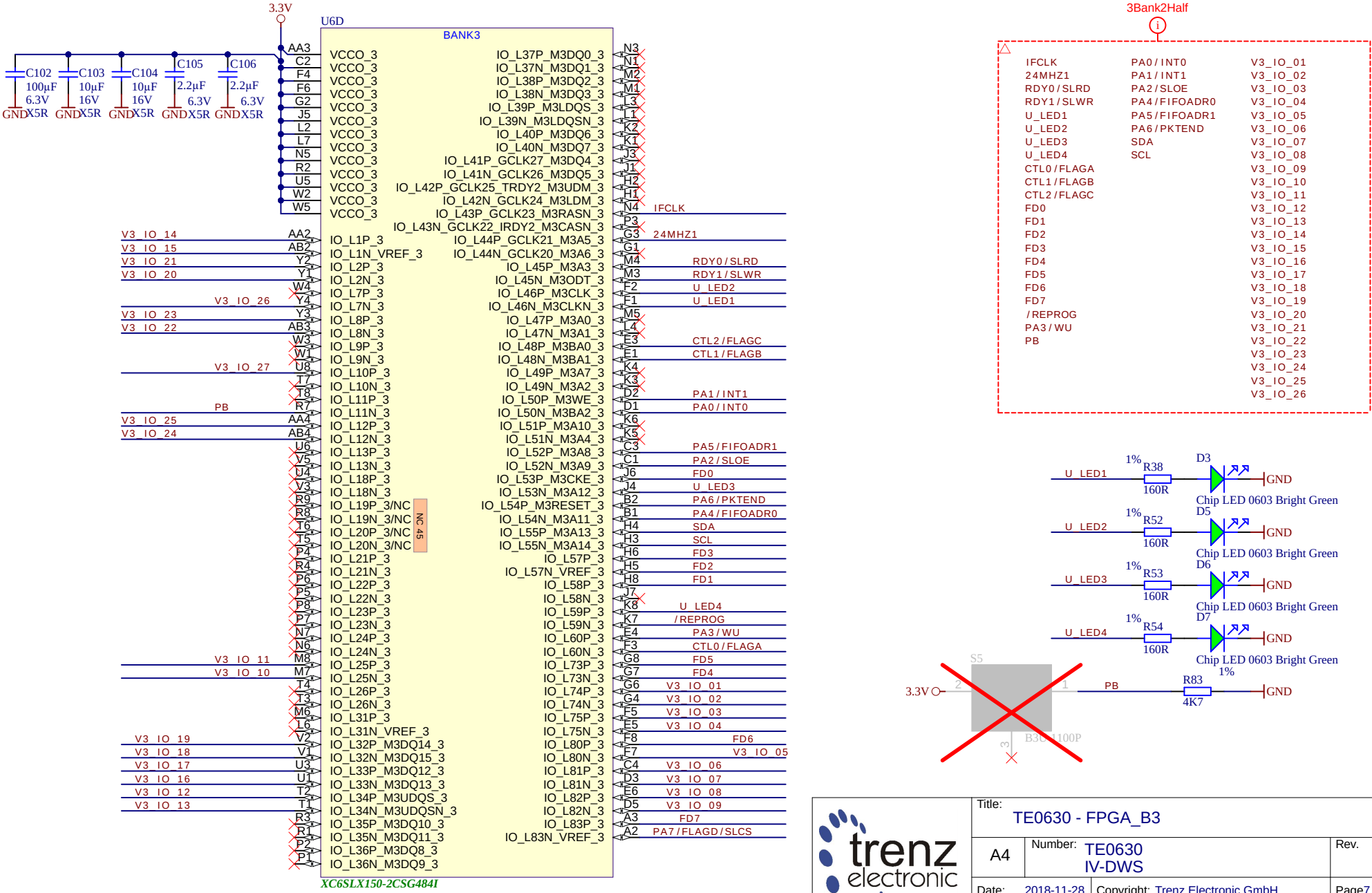
Filename: FPGA_B2.SchDoc			



Title:	TE630 - FPGA_B3		
A4	Number: TE0630 IV-DWS		Rev. 02
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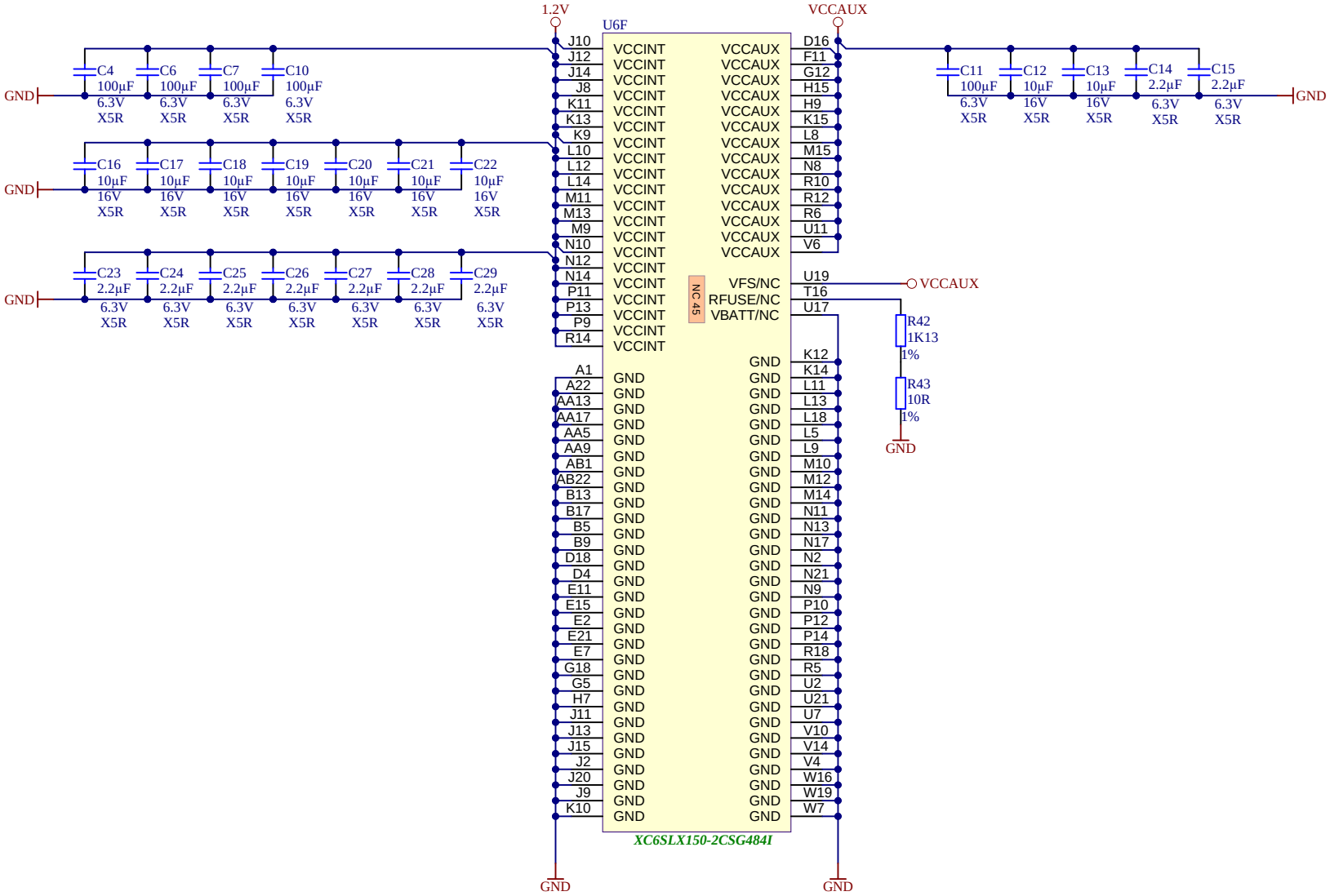
Filename:	FPGA_B3.SchDoc		



Title: TE0630 - FPGA_PWR			
A4	Number: TE0630 IV-DWS		Rev. 02
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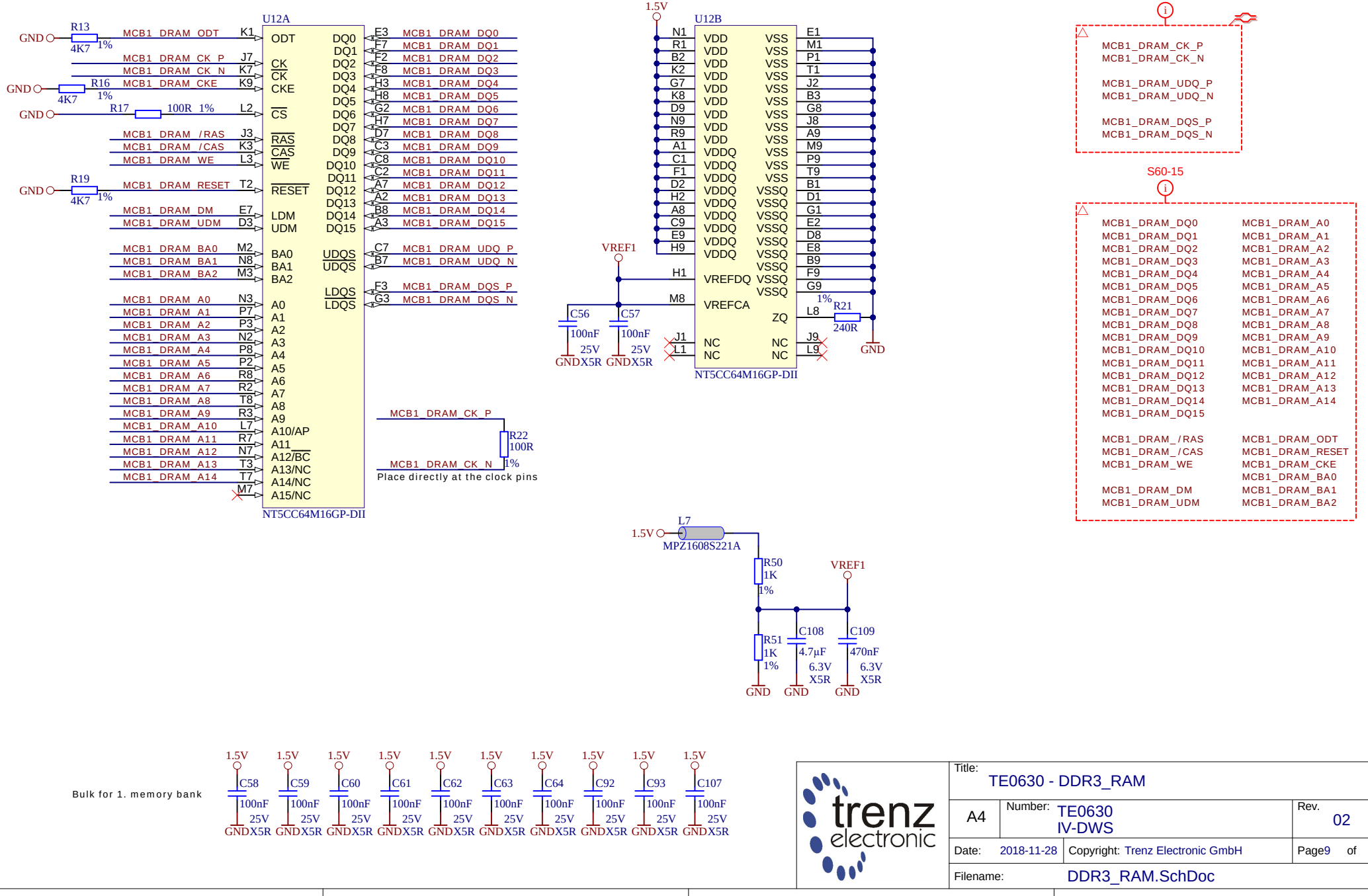
Filename: FPGA_PWR.SchDoc			



Title: TE0630 - DDR3_RAM			
A4	Number: TE0630 IV-DWS		Rev. 02
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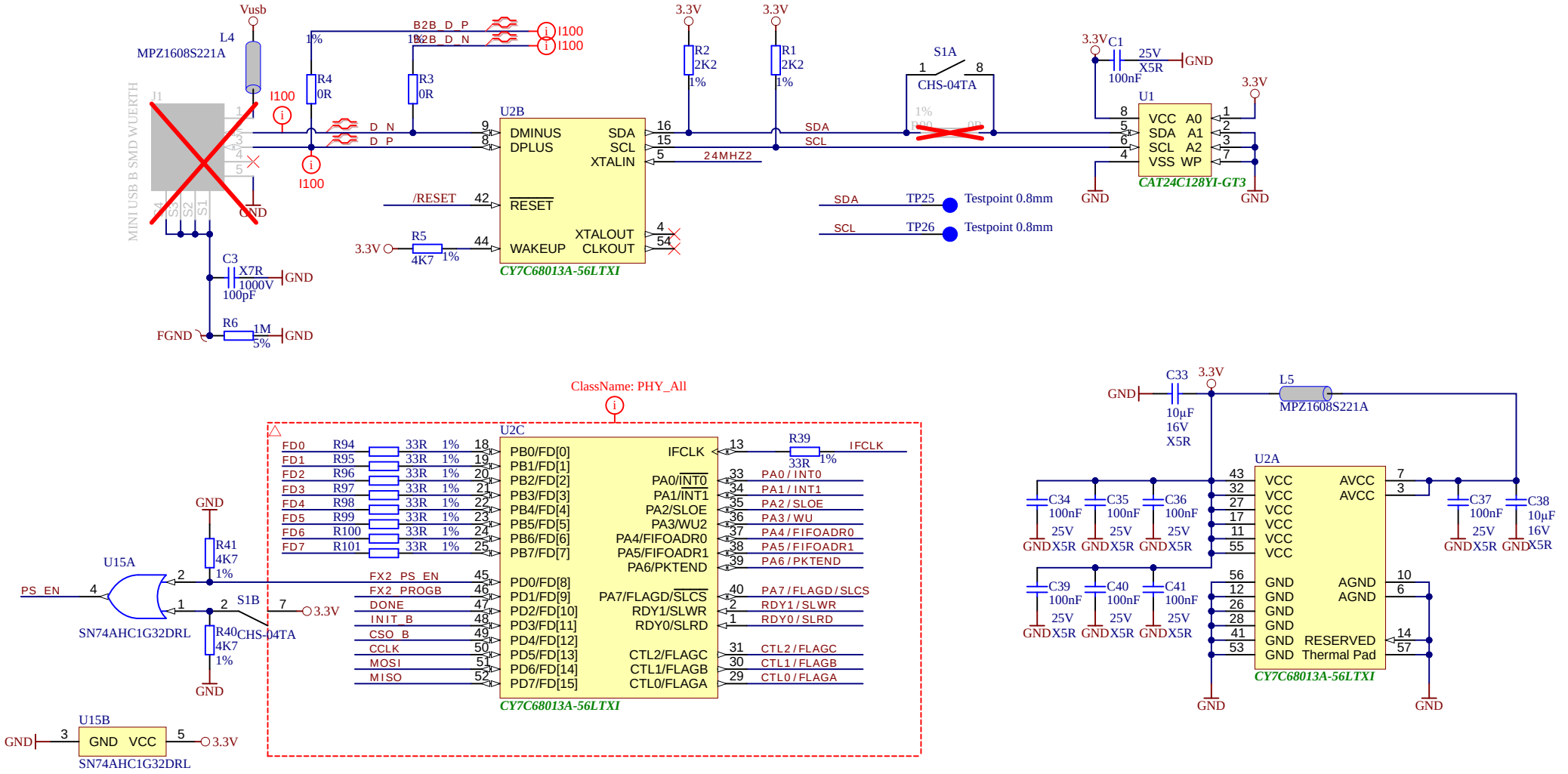
Filename: DDR3_RAM.SchDoc			



trenz electronic	Title: TE0630 - USB		
	A4	Number: TE0630 IV-DWS	Rev. 02
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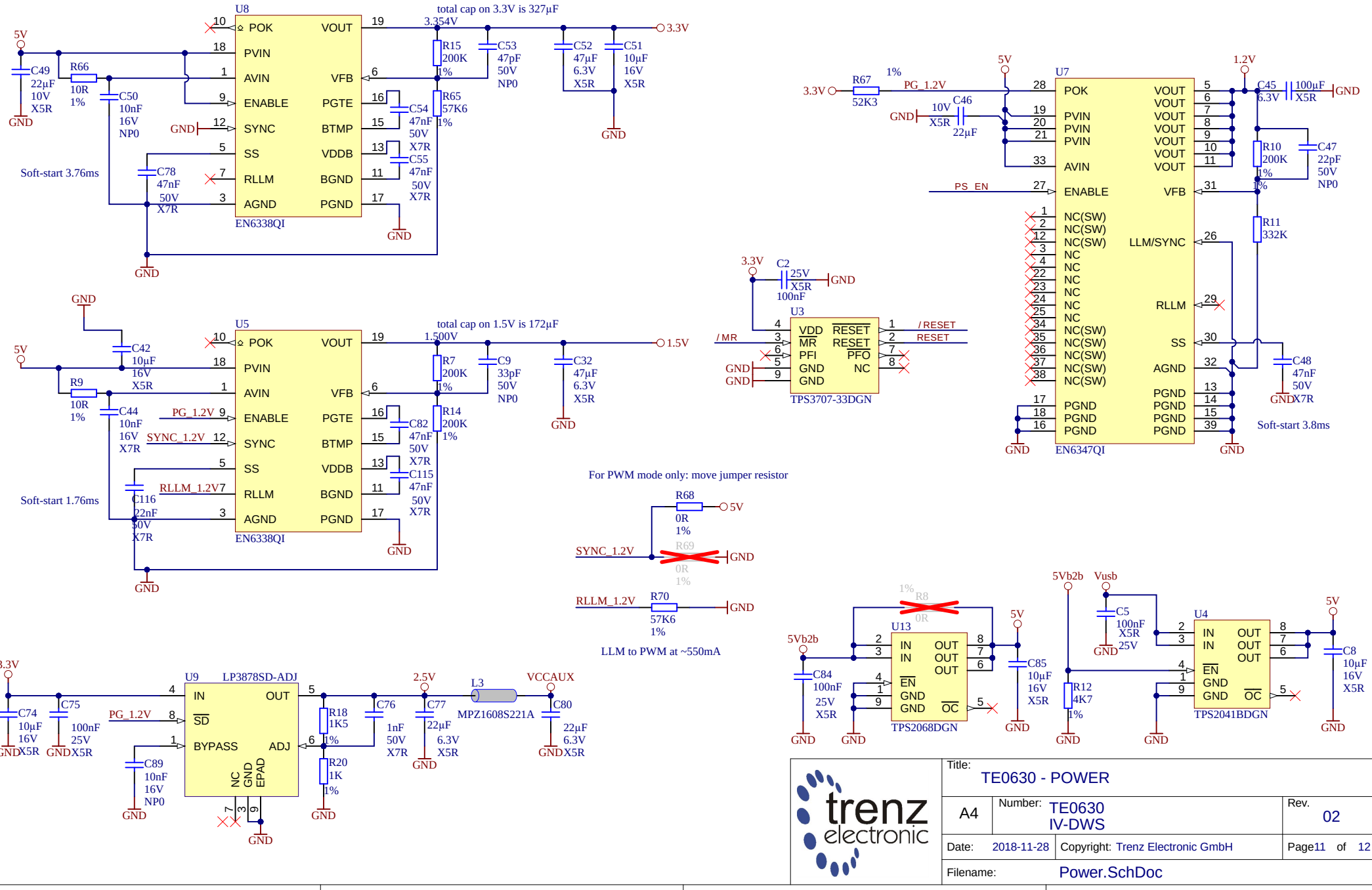
	Filename: USB.SchDoc		



Title: TE630 - POWER			
A4	Number: TE0630 IV-DWS		Rev. 02
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Filename: Power.SchDoc			

CHANGES REV01 to REV02

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1) Replaced U8 by EN6338QI
2) Rerouted nets around U8, results in new track length for:
   Signal V3_IO_06 15.2475 mm (was 15.0047mm)
   Signal V3_IO_07 16.0151 mm (was 15.0452 mm)
3) Replaced U5 by EN 6338QI
4) PS_EN now via OR gate
5) Replaced obsolete Diodes D1, D2 D4, by BAT54A
6) Fixed Footprint of U10 according to datasheet
7) Update from LIB
8) Rearranged Testpoints
9) Added Traceability Pad
10) Replaced S5 by smaller PB AN26337
11) Hardware revision coding updated to Rev02

CHANGES REV02

2020-03-30
1) Flash change (U14)

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Title: TE0630 - Changes list		
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