

			implemented alternative connections															
MUX Value			0x00		0x01		0x02		0x03		0x04		0x05		0x06		0x07	
pmc address offset	MUX_CTRL_IN	GPIO	MIO	Connected	ALT0	Connected	ALT1	Connected	ALT2	Connected	ALT3	Connected	ALT4	Connected	ALT5	Connected	RPi Rev2.0	Connected
0x0	<7:0>	GPIO0	MIO0	YES	SDA0	NO	SA5	NO	<reserved>	NO		NO		NO		NO	SDA0	NO
0x4	<15:8>	GPIO1	MIO1	YES	SCL0	NO	SA4	NO	<reserved>	NO		NO		NO		NO	SCL0	NO
0x8	<23:16>	GPIO2	MIO2	YES	SDA1	YES	SA3	NO	<reserved>	NO		NO		NO		NO	SDA1	YES
0x12	<31:24>	GPIO3	MIO3	YES	SCL1	YES	SA2	NO	<reserved>	NO		NO		NO		NO	SCL1	YES
0x16	<39:32>	GPIO4	MIO4	YES	GPCLK0	NO	SA1	NO	<reserved>	NO		NO		NO	ARM_TDI	NO	GPIO_GCLK	NO
0x20	<47:40>	GPIO5	MIO5	YES	GPCLK1	NO	SA0	NO	<reserved>	NO		NO		NO	ARM_TDO	NO	CAM_CLK	NO
0x24	<55:48>	GPIO6	MIO6	YES	GPCLK2	NO	SOE_N / SE	NO	<reserved>	NO		NO		NO	ARM_RTCK	NO	LAN_RUN	NO
0x28	<63:56>	GPIO7	MIO7	YES	SPI0_CE1_N	YES	SWE_N / SRW_N	NO	<reserved>	NO		NO		NO		NO	SPI_CE1_N	YES
0x32	<71:64>	GPIO8	MIO8	YES	SPI0_CE0_N	YES	SD0	NO	<reserved>	NO		NO		NO		NO	SPI_CE0_N	YES
0x36	<79:72>	GPIO9	MIO9	YES	SPI0_MISO	YES	SD1	NO	<reserved>	NO		NO		NO		NO	SPI_MISO	YES
0x40	<87:80>	GPIO10	MIO10	YES	SPI0_MOSI	YES	SD2	NO	<reserved>	NO		NO		NO		NO	SPI_MOSI	YES
0x44	<95:88>	GPIO11	MIO11	YES	SPI0_SCLK	YES	SD3	NO	<reserved>	NO		NO		NO		NO	SPI_SCLK	YES
0x48	<103:96>	GPIO12	MIO12	YES	PWM0	YES	SD4	NO	<reserved>	NO		NO		NO	ARM_TMS	NO	nc	NO
0x52	<111:104>	GPIO13	MIO13	YES	PWM1	YES	SD5	NO	<reserved>	NO		NO		NO	ARM_TCK	NO	nc	NO
0x56	<119:112>	GPIO14	MIO14	YES	TXD0	YES	SD6	NO	<reserved>	NO		NO		NO	TXD1	YES	TXD0	YES
0x60	<127:120>	GPIO15	MIO15	YES	RXD0	YES	SD7	NO	<reserved>	NO		NO		NO	RXD1	YES	RXD0	YES
0x64	<135:128>	GPIO16	MIO16	YES	<reserved>	NO	SD8	NO	<reserved>	NO	CTS0	YES	SPI1_CE2_N	YES	CTS1	YES	STATUS_LED_N	NO
0x68	<143:136>	GPIO17	MIO17	YES	<reserved>	NO	SD9	NO	<reserved>	NO	RTS0	YES	SPI1_CE1_N	YES	RTS1	YES	GPIO_GEN0	NO
0x72	<151:144>	GPIO18	MIO18	YES	PCM_CLK	NO	SD10	NO	<reserved>	NO	BSCSL SDA / MOSI	NO	SPI1_CE0_N	YES	PWM0	YES	GPIO_GEN1	NO
0x76	<159:152>	GPIO19	MIO19	YES	PCM_FS	NO	SD11	NO	<reserved>	NO	BSCSL SCL / SCLK	NO	SPI1_MISO	YES	PWM1	YES	nc	NO
0x80	<167:160>	GPIO20	MIO20	YES	PCM_DIN	NO	SD12	NO	<reserved>	NO	BSCSL / MISO	NO	SPI1_MOSI	YES	GPCLK0	NO	nc	NO
0x84	<175:168>	GPIO21	MIO21	YES	PCM_DOUT	NO	SD13	NO	<reserved>	NO	BSCSL / CE_N	NO	SPI1_SCLK	YES	GPCLK1	NO	CAM_GPIO / GPIO_GEN2	NO
0x88	<183:176>	GPIO22	MIO22	YES	<reserved>	NO	SD14	NO	<reserved>	NO	SD1_CLK	YES	ARM_TRST	NO		NO	GPIO_GEN3	NO
0x92	<191:184>	GPIO23	MIO23	YES	<reserved>	NO	SD15	NO	<reserved>	NO	SD1_CMD	YES	ARM_RTCK	NO		NO	GPIO_GEN4	NO
0x96	<199:192>	GPIO24	MIO24	YES	<reserved>	NO	SD16	NO	<reserved>	NO	SD1_DAT0	YES	ARM_TDO	NO		NO	GPIO_GEN5	NO
0x100	<207:200>	GPIO25	MIO25	YES	<reserved>	NO	SD17	NO	<reserved>	NO	SD1_DAT1	YES	ARM_TCK	NO		NO	GPIO_GEN6	NO
0x104	<215:208>	GPIO26	MIO26	YES	<reserved>	NO	<reserved>	NO	<reserved>	NO	SD1_DAT2	YES	ARM_TDI	NO		NO	nc	NO
0x108	<223:216>	GPIO27	MIO27	YES	<reserved>	NO	<reserved>	NO	<reserved>	NO	SD1_DAT3	YES	ARM_TMS	NO		NO	GPIO_GEN2 / CAM_GPIO	NO

Description for multiplexer settings:

via AXI:

write (BaseAddress+PMCOffset) MuxValue

via MuxControl:

set MUX_CTRL_IN<x+7:x> MuxValue

Example set GPIO2 Pin to RPi Rev2.0 alternative :

BaseAddress: 0x43C0_0000

PMCOffset: 0x8

MuxControl: <23:16>

MUX Value: 0x07

via AXI (devmem example):

write:

devmem 0x43C00008 32 0x07

read:

devmem 0x43C00008

via MuxControl: